Quantum Mechanical Simulation of Nanoscale Double Gate MOSFET



A thesis submitted to the Department of Electrical and Electronic Engineering

of

Bangladesh University of Engineering and Technology

in partial fulfillment of the requirements for the degree of

MASTER OF SCIENCE IN ELECTRICAL AND ELECTRONIC ENGINEERING

by

Md. Golam Rabbani

DEPARTMENT OF ELECTRICAL AND ELECTRONIC ENGINEERING BANGLADESH UNIVERSITY OF ENGINEERING AND TECHNOLOGY

October, 2007



Declaration

I hereby declare that this thesis has been done by me and it or any part of it has not been submitted elsewhere for the award of any degree or diploma.

09-10-07

(Md. Golam-Rabbani)

The thesis entitled "Quantum Mechanical Simulation of Nanoscale Double Gate MOSFET" submitted by Md. Golam Rabbani, Roll No.: 100506261P, Session: October, 2005 has been accepted as satisfactory in partial fulfillment of the requirements for the degree of 'MASTER OF SCIENCE IN ELECTRICAL AND ELECTRONIC ENGINEERING' on October 9, 2007.

BOARD OF EXAMINERS

(Dr. Quazi Deen Mohd Khosru) Professor

Department of Electrical and Electronic Engineering, BUET

Dhaka-1000, Bangladesh

 $(\sim \cdot \cdot = 0)$

(Dr. S. Shahnawaz Ahmed)

Professor and Head

Department of Electrical and Electronic Engineering, BUET

Dhaka-1000, Bangladesh

3.

(Dr. Md. Shafiqul Islam)

Professor

1.

2.

Department of Electrical and Electronic Engineering, BUET

Dhaka-1000, Bangladesh

0

(Dr. M. Rezwan Khan)Professor and Vice-ChancellorUnited International University (UIU)Dhanmondi, Dhaka, Bangladesh

(Supervisor)

Chairman

2

Member (Ex-officio)

Member

Member. (External)

-ii-

To My Family

Acknowledgement

I would like to express my sincere gratitude to my advisor, Professor Quazi Deen Mohd Khosru for his generous help throughout my M.Sc. study in EEE, BUET. Professor Khosru is an admirable academic_professional. He taught-me not-only the basics of Semiconductor Devices but also the advanced literature necessary for a quality thesis. He impressed me very much by his responsibility as well as his congenial attitude in dealing with students. He always provided timely and warm encouragement and support in difficult times. I especially thank him for his prompt reading and careful critique of my thesis. Throughout my life I will benefit from the experience and knowledge I gained working with Professor Khosru.

This thesis benefited from discussions with several of my colleagues in EEE, BUET-and-Iam in their debt. I especially appreciate Md. Kawsar Alam for his stimulating discussions and enthusiasm about my thesis and Sishir Bhowmic for his help in the write-up-and-discussion on finite difference method. I heartily thank Professor Shohidul Hasan and Professor S. P. Majumder, Head of EEE department, for simulation facilities respectively in Robert Noyce Simulation Lab (RNSL) and EEE Department Computer Lab.-

Finally Lowe more than words can describe to my family. This thesis is dedicated to them. But truly acknowledging them is certainly beyond my knowledge in English and it would be equally impossible even if I could do that in my native language.

Contents

Page
knowledgement v
t of Tables viii
ix ix
stract xi
Introduction 1
1.1 Overview of the Problem1
1.2 Scaling Devices to Their Limits
1.3 Overview of the Thesis
2D Quantum Simulation of Nanoscale Double Gate MOSFET 7
2.1 Solution of 2D Schrödinger Equation
2.1.1 _ The coupled mode space (CMS) approach
2.1.2 The uncoupled mode space (UMS) approach12
2.1.3 A fast uncoupled mode space (FUMS) approach13
2.1.4 The nonequilibrium Green's function (NEGF)14
2.1.5 Calculation of charge density and current
2.1.6 Solution of 1D Schrödinger Equation18
2.2 Solution of 2D Poisson Equation
2.2.1 Domain Discretization24
2.2.2 Elemental Interpolation Function25
2.2.3 -Formulation of the System of Equations
2.2.4 Assembly to form the System of Equations

2.2.5	Imposition of Actual Boundary Condition	30
2.2.6	Solution of the System of Equations	.31

3	Simulation Results and Discussions	32
	3.1 Eigen Energies and Eigenfunctions (Wavefunctions)	33
	3.2 Electron Density Profile	38
	3.3 /3D Conduction Band Edge Profile	42
	3.4 Drain-to-Source Current	44
	3.5 Validity of FUMS approach	46
	3.6 Comparison between femnet and nanoMOS simulation results	47

4	Conclusion	51
	4.1 Summary	.51
	4.2 Future Work	.52

Bibliography				
	100	1	140	-

.

....

Appendix A

59

54

List of Tables

,

	Ó				
3.1:	Important physical	parameters value	es used in the sin	nulation	

.

List of Figures

2.1	Symmetrical double gate_MOSFET structure_studied in this_work. The source/drain	
	doping is around 1×10^{20} cm ⁻³ , the channel is intrinsic and the transistor is assumed to	
	be wide (y-dimension is treated as very large and only an x-z-plane is considered in the	
	simulation)	8
2.2	Self-energy concept. (a) The interaction of a device with a reservoir can be	
	represented by a self-energy matrix Σ . (b) Self-energy of the device itself ($\Sigma_{\rm S}$) along	
	with the self-energies of source (Σ_1) and drain (Σ_2)	14
2.3	One-dimensional domain subdivided into linear elements. (a) Element and global	
	node numbers. (b) Linear element with local node numbers	19
2.4	One dimensional linear interpolation functions	20
2.5	Domain discretization of the 2D DG MOSFET structure. Global node numbers and	
	the element numbers are also shown	25
2.6	Linear interpolation functions for a triangular element. (a) N_1^e , (b) N_2^e , (c) N_3^e . The	
	planar surfaces of the functions are shaded	26
2.7	Actual boundary conditions	30
3.1	Eigen energy levels along the transport direction (unbiased condition)	33
3.2	Eigen energy-levels along-the transport direction (biased-condition). From source to-	
	drain the eigen energy levels fall although there is an increase near the source-channel	
	contact	34
3.3	The first (groundstate) eigen energy variation along the channel with Vg. Vd is (clock	
	wise from top-left-corner) 0.0V, 0.1V, 0.3V, and 0.5V.	35_
3.4	The first (groundstate) eigen energy variation along the channel with Vd. Vg is (clock	
	wise from top left corner) 0.0V, 0.1V, 0.3V, and 0.5V	36
3.5	The unprimed and primed eigen functions along y-direction t the source or drain end.	
	Curves without symbol are for unprimed valley while those with symbol are for	
	primed valleys. Solid line is for ground state and dashed line is for the next higher	
	state	37

ø

ļ

3.6	The unprimed and primed eigen functions along y-direction at the middle of the	
	channel. Curves without symbol are for unprimed valley while those with symbol are	
	for primed valleys. Solid line is for ground state	37
3.7	2D electron density of different subbands along the channel (unbiased condition)	38
3.8	2D electron density of different subbands along the channel (biased condition)	39
3.9	2D electron density variation along the channel with Vg. Upper part for Vd=0.0V and	
	lower part for Vd=0.5V	40
3.10	2D electron density variation along the channel with Vd. Upper part for Vg=0.0V and	
	lower part for Vg=0.5V	40
3.11	Variation of 3D electron density with silicon thickness, t_{si} . (a), (b), and (c) show the	
	3D density for t_{si} values 1.5, 3.0, and 5.0 nm, respectively; (d) shows the ground state	
	eigenfunction along a vertical slice	41
3.12	Conduction band profile; (a) 3D, Vg=0.3V, Vd=0V, (b) 3D, Vg=0.3V, Vd=0.3V, (c)	
	1D, Vg=0.3V, Vd=0V, (d) 1D, Vg=0.3V, Vd=0.3V	42
3.13	3D conduction band profile at different Vg. Vd=0.5V	43-
3.14	3D conduction band profile at different Vd. Vg=0.5V	43
3.15	I_{DS} -V _{DS} characteristic of a DG MOSFET with L_g =10.0 nm and t_{si} =3.0 nm	-44
3.16	I_{DS} variation with silicon thickness (t _{si}). V_{GS} =0.5V	45
3.17	I_{DS} vs. V_{DS} characteristic dependence on maximum number of subbands considered in -	
	the calculation	45
3.18	Comparison-of-subband-levels-and-their populations-produced-by FUMS-and UMS approach.	
	Solid line represents results of FUMS and symbol represents results of UMS	46
3.19	Comparison of I _{DS} vs. V _{DS} profiles produced by FUMS and UMS approach. Solid line	
	represents results of FUMS and symbol represents results of UMS	47
3.20	Comparison between eigen energy levels produced by femnet (blue) and nanomos	40
	(red). (a) unprimed subbands, (b) primed subbands.	48
3.21	Comparison between 2D subband populations produced by femnet (blue) and	
	nanomos (red). (a) unprimed subbands, (b) primed subbands	49
3.22	Comparison between drain currents produced by femnet (solid filled circle in blue)	50
	and nanomos (solid open circle in red)	50

• .

.

(· · ·

Abstract

This thesis discusses device physics and simulation issues of symmetrical nanoscale doublegate transistors at the quantum level. The purpose of the thesis is threefold 1) an understanding of the basic physics of symmetrical double-gate_MOSFETs (or any nanoscale_device_for-that matter), 2) implementation of appropriate physics and methodology in nanoscale device modeling, 3) development of a new TCAD (technology computer aided design) tool for quantum. level device simulation. We concentrate on the technical issues by investigating a double-gate structure, which has been widely accepted as the ideal device structure for ultimate-CMOS scaling in the future. After a brief look at the basic physics of symmetrical double-gate MOSFET, we focus on quantum effects and non-equilibrium ballistic transport in extremely scaled transistors (in contrast to quasi-equilibrium, scattering-dominant transport in long channel devices), where a non-equilibrium Green's function (NEGF) formalism has been used to deal with the quantum transport problem. The self-consistent solution of Poisson's equation and NEGF in nanoscale device modeling-is-shown to be very promising and general. Results have_ been compared with those of a standard simulator and it has been found that the developed simulator produces similar results but requires much less time because of the use of our fast uncoupled mode space (FUMS) approach. Finite element method (FEM) has been used in discretizing the relevant PDEs. FEM can handle complex geometry more easily than the usually used finite difference (FDM) method. FEM along with FUMS makes nanoscale device simulation efficient and flexible.

-xi-



-CHAPTER 1

Introduction

1.1. Overview of the Problem

CMOS technology has been proven as one of the most important achievements in modern engineering history. For decades, progress in device scaling has followed an exponential curve: device density on a microprocessor doubles every three years. This is known as the Moore's law [1]. The minimum dimension size of a single device for present day technology is less than 100 nm in gate-length. Continued success in device_ scaling is necessary for further development of the semiconductor industry in the years to come. A group of leading companies publishes their projections for the next decade in the recent International Technology Roadmap for Semiconductors (ITRS-05) [2]. The roadmap projects a device gate-length down to ~30 nm around 2014 [2]. Scaling beyond 30 nm, however, can be much more difficult and different for it is quite close to the fundamental limit of semiconductor physics. Even if lithographic and etching techniques can provide the necessary dimensions, bulk CMOS will run into a number of short channel effects-associated_with transistor scaling. The short_channel effect (SCE)_is characterized by threshold voltage V_{TH} roll-off, drain induced barrier lowering (DIBL) and sub-threshold swing. As the gate length of a MOSFET is scaled with all other device parameters held constant, sub-threshold swing increases and V_{TH} decreases, which degrade MOSFET performance. The ratio of on current to off current (I_{ON} / I_{OFF}) is reduced, giving designers a tradeoff between circuit speed and static power dissipation. Without doubt, we are facing numerous challenges, both practically and theoretically. Device simulation requires new theory and approaches to understand device physics and to design devices at the sub-30nm-scale. Efforts have been put forth in-recent years [3-7], but much more is needed.

The principle objectives of this_thesis_are: 1)_to_understand_the basic_physics-of symmetrical double-gate MOSFETs, 2) to implement appropriate physics and methodology in nanoscale device modeling, and 3) to develop a new TCAD (technology

1.2. Scaling Devices to Limits

There are two primary device structures that have been widely studied and used in CMOS technology. One is the bulk structure, where a transistor is directly fabricated on the semiconductor substrate. The other one is called SOI (silicon-on-insulator), where a transistor is built on a thin silicon layer, which is separated from the substrate by a layer of insulator. The bulk structure is relatively simple from a device process point of-view, and it is still the standard structure in almost all CMOS based products. For device scaling, we basically try to balance two things: device functionality and device reliability. Both of them have to be maintained at a smaller dimensional size. To accomplish this, we need to suppress any dimension related effects or short channel effects (SCEs) as much as possible. SCEs include threshold voltage (V_{TH}) variations versus channel length, typically V_{TH} roll-off at shorter channel lengths. This effect is usually accompanied by degraded subthreshold swing (S), which causes difficulty in turning off a device. SCEs also include the drain-induced barrier lowering (DIBL) effect. DIBL results in a drain voltage dependent V_{TH} , which complicates CMOS design at a circuit level. As a transistor scales, reliability concerns become more pronounced. Unwanted leakage currents can

make the device_fail to function properly. Primarily, there are two kinds of leakages, gate tunneling current and junction tunneling current. Both of them result from extremely scaled dimensions_and high_electric fields. According_to device scaling_physics, increasing channel doping concentration (N_B) can effectively suppress SCEs. Frank *et al.* published their work quantifying the dependence of the scale length on N_B [14]. To a first order approximation, their theory gives the following equation,

$$\Lambda = W_{dm} + (\varepsilon_{Si} / \varepsilon_{I})T_{I}$$
(1.1)

where Λ -is-the scale length, W_{dm} is the maximum-channel-depletion depth, T_1 is the insulator thickness, and $\varepsilon_{Si} / \varepsilon_1$ is the ratio of dielectric constants of silicon and the insulator. W_{dm} can be directly related to N_B (see for example [15]). Depending on the complexity of the channel doping profile, this theory_predicts that the minimum design length L_G lies between Λ to 2Λ . It is quite clear in Eq. (1.1) that high N_B results in reduced W_{dm} , therefore a shorter scale length Λ . Of course, thinner T_L or higher ε_L also helps device scaling.

Device scaling has come a long way. In the early days, L_G was relatively long, a low uniform N_B could be used providing satisfactory immunity of SCEs. A low N_B gives a small body effect- coefficient, which- improves- the subthreshold_swing- [15]. As-thechannel length decreases, a retrograde or ground plane doping profile can be introduced [16-17]. This doping profile has a low doping region near the Si/Oxide interface, but a high doping region underneath. The top region provides better body effect, while the -bottom-region-suppresses-SCEs. To achieve even shorter-channel lengths, a ground planeprofile is not enough, a more complicated doping profile has to be added, namely the super halo [18]. In this case, high gradient halo dopings_are_formed_next_to-the source/drain junction region. These heavily doped regions can effectively protect the source end of the channel region from the influence due to the electric fields from the drain diffusion region.

 $\langle \cdot \rangle$

As the channel length varies around the nominal L_G , a shorter length causes the halo regions to merge, ending up with higher N_B , which resists V_{TH} rolloff. By using the ground plane and halo-doping-profiles, simulations show that the bulk structure can be--scaled down to ~25 nm regime [18]. Beyond that, device scaling of the bulk structure is limited by severe degradation of junction leakage which is caused by the high built-in fields, and can not be avoided in the wake of the super halo engineering. Partially depleted SOI MOSFETs-scale-in-a very-similar-manner-as-the-bulk-devices do. The___ buried oxide layer in a SOI device can provide superior electric isolation between the active device region and the substrate region. This property is considered -a-big improvement over bulk devices. Body isolation, however, also results in charge buildup (majority carriers)-within-the body region, which gives rise to the unwanted floating bodyeffect (FBE) [19]. A fully depleted SOI MOSFET can help relieve the FBE, but a fully depleted single gate SOI MOSFET_is_not_considered_a_desired_structure_for_scaling.-A single gate SOI device typically has a thick buried oxide layer, which can not terminate any electric lines from the drain end, leaving the source vulnerable to the influence of the drain [17, 20]. All recent studies indicate that the ultra-thin body double gate (DG) SOI MOSFET is the ideal device structure for ultimate_scaling_[4, 21-22]. In an ultra-thin body /DG MOSFET, the second gate electrode can significantly suppress the SCEs. Referring to Eq. 1.1, and noting that W_{dm} can be approximated by $T_{Si}/2(T_{Si})$ is the silicon body thickness), when T_{Si} is scaled to nanometer thicknesses (close to T_I), clearly the scale length will downsize into the nanometer regime. It should be also noticed that high body doping is not needed here, so the band-to-band tunneling junction leakage is no longer a big concern. Moreover, the use of ultra-thin bodies will result in reduced metallurgical junction perimeter, therefore low junction capacitance. The bodies are typically lightly doped, giving other advantages: 1) there is barely room for the FBE to come into play, 2) the V_{TH} variation due to dopant fluctuations can be eliminated, 3) close-to-ideal subthreshold swing (60-mV/dec) can be achieved, 4) severe mobility degradation due to ion scattering might be avoided. From a technical point of view, DG MOSFETs are difficult to build. Gate self-alignment is hard to achieve. A misaligned gate will cause high overlap capacitances-on-one-side-of-the-gate, and large underlap

junction resistances on the other side of the gate. Recent works show that clever process designs can help get rid of gate misalignment [23-24]. Extension region resistances pose another concern-in-DG-MOSFET-design. Due to-the use-of-ultra-thin_bodies, these resistances can be very high, limiting device performance. The proposed solution is to use fanned out source/drain regions as close as possible to the channel region [25]. The use of ultra-thin bodies also leaves limited room for adjusting V_{TH} with body doping. Gate stack engineering has to be done to obtain an appropriate V_{TH} , either by employing new contact materials with desirable workfunctions, or maintaining an offset voltage between the two gate electrodes to mimic a different workfunction_[24-26]. Quantum effects (subbband splitting) can become significant as the confinement of carriers becomes stronger within ultra-thin bodies, translating to sensitivity of V_{TH} to the body thickness. This fundamental physics effect poses an additional difficulty to control V_{TH} in ultra-thin bodes. (It is worthwhile to point out that this subband splitting effect-will increase the band gap between lowest electron subband and highest hole subband, which may considerably suppress the band-to-band tunneling leakage in ultra-thin-silicon--bodies.) Despite the existence of numerous difficulties, the excellent scaling capability demonstrated by the ultra-thin body DG structure can never be underestimated. For-this reason, therefore, this thesis will be concentrated on a study of ultra-thin body DG MOSFETs.

1.3. Overview of the Thesis

The thesis is organized as follows. In Chapter 2, we describe the numerical techniques used_in_developing a_2D-simulator_for-nanoscale_double-gate_MOSFETs___ First, we implement a quantum ballistic transport model by employing fast uncoupled mode space (FUMS) approach in the nonequilibrium Green's function (NEGF) formalism. Then the 2D Poisson equation is solved using the Gummel iteration. All the relevant equations (1D Schrödinger equation, 1D NEGF and 2D Poisson equation) are discretized using the finite element method (FEM).

5

In Chapter 3, we_present simulation results of our work._Different properties_of the nanoscale DG MOSFET are demonstrated through relevant plots. We provide eigen energy and eigen-function-profiles, electron-density profiles,-conduction band edge plots,- and drain current profiles taking into account bias, thickness and maximum number of subbands dependence._We_also_demonstrate validity_of_the_FUMS_approach_by comparison with the uncoupled mode space (UMS) approach. Finally, results of our simulation process are compared-with-those-from a-standard simulator.-

Chapter 4_summarizes the conclusions of this_research, and_lists_a_few_potential directions for future work. A sample 2D mesh generator code developed for the simulation and a flow chart of the simulation process are given in Appendix A.

į

برز. مجرب می د

CHAPTER 2

2D Simulation of Nanoscale Double Gate MOSFET

Recently Taur *et al.* [29-31] has developed analytical solutions of charge, potential and drain-current for symmetric undoped double-gate MOSFETs. Quantum effects have been neglected in the development of those analytical formulations. But as transistor dimensions are scaled down, quantum effects, which affect the threshold voltage (confinement), gate capacitance (charge centroid shift), off-current (source barrier tunneling) and gate leakage begin to manifest themselves and semiclassical methods or the analytical models that disregard these effects become inadequate in capturing the physics of ballistic transport. The Nonequilibrium Green's Function (NEGF) approach [35-36] provides a general simulation framework for quantum mechanical simulation (with or without scattering) and it will be applied to model electron transport in this work.

In this chapter, we present the two-dimensional (2D)-quantum-mechanical-simulation — — of nanoscale double gate (DG) MOSFETs in the finite element method (FEM domain) approach. Effective mass approximation (whose validity for nanoscale device simulation has been established in [37]) is used and we determine the upper performance limit of the devices so that scattering is disregarded. For a method-for-incorporation of scattering into — the simulation refer to [38]. For the solution of 2D Schrödinger equation, instead of the usually used real-space approach, we use the mode space approach [39]-which has been shown to be very efficient for the device geometry simulated in this work. The geometry also permits us to decouple the modes thereby greatly reducing the simulation cost. As the silicon channel thickness is less than 5 nm, strong quantum confinement is present so that only a few lower subbands are occupied and need to be treated. We further show that due to the small device size there exists a strong co-relation between the potentials at different points of the device and an averaging approach can be utilized to speed up the simulation without sacriffeing accuracy.

7

2.1 Solution of 2D.Schrödinger Equation

In this section we show how the 2D problem can be rendered to a number of 1D problems through the use of the novel mode space [39] (subband decomposition) _approach. Utilizing the convenient structural symmetry of DG geometry the modes can be uncoupled and only a few are needed consideration, which greatly improves simulation efficiency. The simulation can be further speeded up through the use of an averaging approach called the fast uncoupled mode space (FUMS) approach.

2.1.1 The coupled mode space (CMS) approach

The double gate MOSFET geometry that we use is shown in Fig. 2.1. We assume an intrinsic device. The device is wide in the y-direction and no confining potential exists in this direction, so the wave functions in this direction are plane waves and the 2D simulation is performed in the x-z plane as shown in Fig. 2.1.

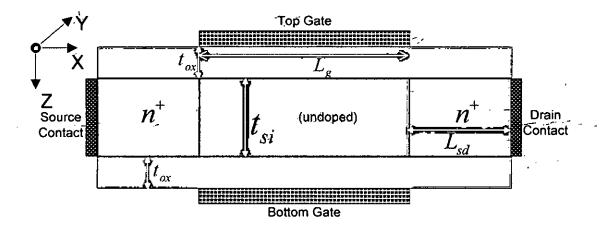


Fig. 2,1 Symmetrical_double_gate MOSFET_structure_studied_in_this work. The_source/drain doping is around 1×10^{20} cm⁻³, the channel is intrinsic and the transistor is assumed to be wide (y-dimension is treated as very large and only an x-z plane is considered in the simulation).

Thus the total wave function can be written as $\psi(x, y, z) = \Phi(x, z)e^{ik_y y} / \sqrt{W}$ where W is the width of the device (in y-direction) and $\Phi(x, z)$ is obtained from

$$-\frac{\hbar^2}{2m_x^*}\frac{\partial^2 \Phi(x,z)}{\partial x^2} - \frac{\hbar^2}{2m_z^*}\frac{\partial^2 \Phi(x,z)}{\partial z^2} + E_C(x,z)\Phi(x,z) = (E - E_{ky})\Phi(x,z) = E_I\Phi(x,z)$$
(2.1)

where m_x^* , m_z^* are the electron effective masses in the x-, and z-directions respectively, E is the assumed energy, $E_l = E - E_{ky}$ is the longitudinal energy, and $E_{ky} = \frac{\hbar^2 k_{yz}^2}{2m^*}$.

We can directly discretize Eq. (2.1) in real space but it imposes a heavy computational burden [39]. For MOSFETs with a thin body (<5nm), quantum confinement in the z-direction introduces subbands, and only a few of the lowest subbands are necessary as population of the higher subbands are negligible. Accordingly, we can expand $\Phi(x, z)$ in the subband eigenfunction space,

$$\Phi(x,z) = \sum_{n} \varphi^{n}(x) \xi_{-}^{n}(z;x)$$
(2.2)

where *n* indicates the subband number, $\varphi^n(x)$ are expansion coefficients, $\xi^n(z; x = x_0)$ is the *n*th eigenfunction of the following 1D Schrödinger equation at the slice $x = x_0$ of the DG MOSFET;

$$-\frac{\hbar^2}{2m_z^*}\frac{\partial^2 \xi^n(z;x_0)}{-\partial z^2} + E_C(z;x_0)\xi^n(z;x_0) = E_{sub}^n(x_0)\xi^n(z;x_0)$$
(2.3)

where $E_C(z;x_0)$ is the conduction band profile along z-direction at slice $x = x_0$, $E_{sub}^n(x_0)$ is the *n*th subband energy level due to $E_C(z;x_0)$. According to the property of eigenfunctions, $\xi^n(z;x)$ satisfies the following equation for any x

9

۲ ۲

$$\int_{z} \xi^{m}(z;x)\xi^{n}(z;x)dz = \delta_{m,n}$$
(2.4)

where $\delta_{m,n}$ is the Kronecker delta function. Inserting Eq. (2.2) into Eq. (2.1) and making use of Eq. (2.3) we get;

$$-\frac{\hbar^{2}}{2m_{x}^{*}}\frac{\partial^{2}}{\partial x^{2}}\left[\sum_{n}\varphi^{n}(x)\xi^{n}(z;x)\right] - \frac{\hbar^{2}}{2m_{z}^{*}}\frac{\partial^{2}}{\partial z^{2}}\left[\sum_{n}\varphi^{n}(x)\xi^{n}(z;x)\right] + E_{C}(z;x)\left[\sum_{n}\varphi^{n}(x)\xi^{n}(z;x)\right]$$

$$= E_{I}\left[\sum_{n}\varphi^{n}(x)\xi^{n}(z;x)\right]$$

$$or, -\frac{\hbar^{2}}{2m_{x}^{*}}\frac{\partial^{2}}{\partial x^{2}}\left[\sum_{n}\varphi^{n}(x)\xi^{n}(z;x)\right] + \sum_{n}\left(-\frac{\hbar^{2}}{2m_{z}^{*}}\frac{\partial^{2}}{\partial z^{2}}\xi^{n}(z;x) + E_{C}(z;x)\xi^{n}(z;x)\right)\varphi^{n}(x)$$

$$= E_{I}\left[\sum_{n}\varphi^{n}(x)\xi^{n}(z;x)\right]$$

$$or, -\frac{\hbar^{2}}{2m_{x}^{*}}\frac{\partial^{2}}{\partial x^{2}}\left[\sum_{n}\varphi^{n}(x)\xi^{n}(z;x)\right] + \sum_{n}\varphi^{n}(x)E_{sub}^{n}(x)\xi^{n}(z;x) = E_{I}\left[\sum_{n}\varphi^{n}(x)\xi^{n}(z;x)\right]$$

$$(2.5)...$$

Now we multiply on both sides by $\xi^m(z;x)$ and do an integral in z-direction. According to Eq. (2.4), we obtain the following 1D Schrödinger equation:

$$-\frac{\hbar^2}{2m_x^*} \left(\sum_n a_{mn}(x) \right)^2 \frac{\partial^{2-}}{\partial x^2} \varphi^m(x) - \frac{\hbar^2}{2m_x^*} \sum_n c_{mn}(x) \varphi^m(x) - \frac{\hbar^2}{m_x^*} \sum_n b_{mn}(x) \frac{\partial}{\partial x} \varphi^n(x) + E_{sub}^m(x) \varphi^m(x) = E_1 \varphi^m(x)$$

where----

$$a_{mn}(x) = \int_{z} \xi^{m}(z;x)\xi^{n}(z;x)dz = \delta_{mn}$$
(2.7a)

(2.6)

$$b_{mn}(x) = \int_{z} \xi^{m}(z;x) \frac{\partial}{\partial x} \xi^{n}(z;x) dz$$
(2.7b)

and

$$c_{mn(x)} = \int \xi^m(z;x) \frac{\partial^2}{\partial x^2} \xi^n(z;x) dz$$
(2.7c)

where,

$$h_{mn} = \delta_{m,n} \left[-\frac{\hbar^2}{2m_x^*} \frac{\partial^2}{\partial x^2} + E_{sub}^m(x) \right] - \frac{\hbar^2}{2m_x^*} c_{mn}(x) - \frac{\hbar^2}{m_x^*} b_{mn}(x) \frac{\partial}{\partial x}$$
(2.9)
(m, n=1, 2,...,M)

By using the coupled mode space (CMS) approach, the size of the device Hamiltonian *H* has been reduced to $(M \times .N_X) \times (M \times N_X)$ (N_X is the number of nodes in -x-direction, and the mode number-*M*-that we need is normally less than 3 for the DG. MOSFET structure we simulate), which is much smaller than that required in the real space representation, $(N_Z \times N_X) \times (N_Z N_X)$ (N_Z is ~40 for the device structures simulated in this work).

11

2.1.2 The uncoupled mode space (UMS) approach

$$\xi^m(\overline{z};x) = \overline{\xi}^m(z) \tag{2.10}$$

or
$$\frac{\partial}{\partial x} \xi^m(z;x) = 0 \quad (m=1,2,\ldots,M),$$
 (2.11)

which when substituted in Eq. (2.7) results in $b_{mn}(x) = 0$ and $c_{mn}(x) = 0$ (*m*, n=1,2,...,M). Using the results in Eq. (2.9), we obtain $h_{mn} = 0$ ($m \neq n$ and *m*, n=1,2,...,M), which means that the coupling between the modes is negligible (all the modes are uncoupled). Thus the device Hamiltonian *H*-becomes a block-diagonal matrix,

where,
$$h_{mn} = \delta_{m,n} \left[-\frac{\hbar^2}{2m_x^*} \frac{\partial^2}{\partial x^2} + E_{sub}^m(x) \right]$$
 (2.13)

12

2.1.3 A fast uncoupled mode space (FUMS) approach

As mentioned earlier, for UMS approach, we need to solve N_X 1D Schrödinger equations in a self-consistent loop to obtain the electron subbands and eigenfunctions. This part of the simulation takes most of the simulation time. To increase the simulation efficiency, we introduce an FUMS approach, which involves only one 1D Schrödinger equation in the z-direction and still provides excellent computational accuracy as compared to UMS approach.

Recall the assumption made in Sec. 2.1.2 that the eigenfunctions $\xi^m(z;x)$ are almost invariant along the x direction, $\xi^m(z;x) = \xi^m(z)$. Now we suppose that the average wave functions $\overline{\xi^m(z)}$ are the eigenfunctions of the following 1D Schrödinger equation

$$\left[-\frac{\hbar^2}{2m_z^*}\frac{\partial}{\partial z^2} + \overline{E_C(z)}\right]\overline{\xi^m(z)} = \overline{E_{sub}^m\xi^m(z)}$$
(2.14)

Here the average conduction band edge $\overline{E_C(z)}$ is obtained as

$$\overline{E_C(z)} = \frac{1}{L_x} \int_x E_C(x, z) dx$$
(2.15)

where L_x is the total length of the simulated DG MOSFET (including the source/drain extensions). After computing the eigenvalues $\overline{E_{sub}^m}$ and eigenfunctions $\overline{\xi^m(z)}$ of this Schrodinger equation, we use the first-order stationery perturbation theory to obtain the subband profile-as [40]

$$E_{sub}^{m}(x) = \overline{E_{sub}^{m}} + \int_{z^{-}} E_{C}(x;z) \left| \overline{\xi^{m}(z)} \right|^{2} dz - \int_{z} \overline{E_{C}(z)} \left| \overline{\xi^{m}(z)} \right|^{2} dz$$
(2.16)

The use of FUMS approach highly improves the efficiency of the simulation and makes it a practical model for extensive device simulation and design.

2.1.4 The nonequilibrium Green's function (NEGF)

After the device Hamiltonian H is obtained, we can calculate the electron density and ______ current using the non-equilibrium Green function (NEGF) approach [35, 10]. NEGF is a very general formalism in that it is equally _applicable_for_DG -MOSFET,-nanowire, molecular transistor, etc. Here we present the relevant equations for our particular case. The retarded Green's function for mode m is defined as [10]

$$G^{m}(E) = \left[E_{I}S - h_{mm} - \sum_{S}^{m}(E) - \sum_{1}^{m}(E) - \sum_{2}^{m}(E)\right]^{-1}$$
(2.17)

where *m* represents mode/subband number, E_1 is the longitudinal energy, S is a discretization method dependent matrix defined below in Eq. (2.18), h_{mm} is defined by Eq. (2.13), Σ_s^m is called the self energy (potential) that accounts for scattering inside the device itself (for the ballistic case, which is assumed in this work, it is a null matrix.), Σ_1^m (Σ_2^m) is the self energy (potential) caused by the coupling between the device and the source (drain) reservoir or contact, as shown below in Fig. 2.2.

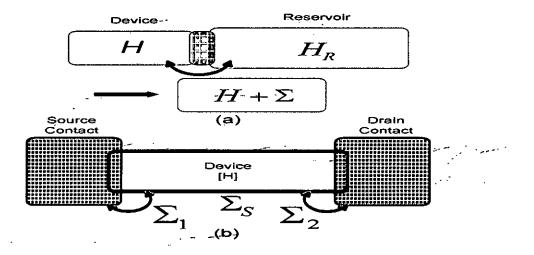


Fig. 2.2 Self-energy concept. (a) The interaction of a device with a reservoir can be represented by a self-energy matrix Σ . (b) Self-energy of the device itself (Σ_s) along with the self-energies of source (Σ_1) and drain (Σ_2) reservoirs. (From [35])

If we discretize the equations by the finite element method (FEM), then S is an $N_X \times N_X$ matrix given by [40]

where *a* is the discretization grid spacing along the x-direction. The self-energies, Σ_1^m and Σ_2^m , are defined as [40]

and

where $t = \hbar^2 / 2m_x^* a^2$ and $k_{m,1} [\pi_{m,N_x}]$ is given by $E_1 = E_{sub}^m (x = 0) + 2t(1 - \cos k_{m,1}a)$ $[E_1 = E_{sub}^m (x = (N_x - 1)a) + 2t(1 - \cos k_{m,N_x}a)].$

2.1.5 Calculation of charge_density_and-current

١

After the retarded Green's function G(E) at a given energy E has been calculated using Eq. (2.17), the spectral density functions due to the source/drain contacts can be obtained as [40] (mode number *m* has not been shown for clarity)

$$A_{1}(E) = G(E)\Gamma_{1}(E)G^{+}(E), A_{2}(E) = G(E)\Gamma_{2}(E)G^{+}(E),$$
(2.21)

where '+' means transpose conjugate and $\Gamma_1(E) \equiv j[\Sigma_1(E) - \Sigma_1^+(E)]$ and $\Gamma_2(E) \equiv j[\Sigma_2(E) - \Sigma_2^+(E)]$. Γ_1 and Γ_2 are known as broadening functions [35], which determine electron exchange rates between the active device region and the source/drain reservoirs at energy *E*. Although the device itself may be in a non-equilibrium state, electrons are injected from the equilibrium source/drain reservoirs. Note that the spectral functions are N_X by N_X matrices and the diagonal entries represent the local density-of-states (LDOS) at each node. The source related spectral function is filled up according to the Fermi energy in the drain contact.

The 2D electron density matrix is obtained as [41]-

$$n(E_{i}) = \frac{1}{2\pi a} \int_{0}^{+\infty} \frac{2}{\pi \hbar} \sqrt{\frac{m_{y}^{*}}{2E_{k_{j}}}} [f(\mu_{1} - E_{i} - E_{k_{j}})A_{1} + f(\mu_{2} - E_{i} - E_{k_{j}})A_{2}] dE_{k_{j}}$$
(2.22a)

where f is the Fermi-Dirac function (for an analytical expression, see [42]), $\mu_1(\mu_2)$ is source (drain) contact chemical potential, and $\frac{2}{\mu\hbar}\sqrt{\frac{m_y^*}{2E_{k_f}}}$ represents the transverse mode state density (including spin degeneracy). Since the spectral functions depend on the longitudinal energy only, they can be moved out of the integration sign. Therefore, Eq. (2.22a) reduces to [41]

$$n(E_{l}) = \frac{1}{\hbar a} \sqrt{\frac{m_{y}^{*} k_{B} T}{2\pi^{3}}} [\mathfrak{I}_{-1/2}(\mu_{1} - E_{l})A_{1} + \mathfrak{I}_{-1/2}(\mu_{2} - E_{l})A_{2}], \qquad (2.22b)$$

where the Fermi-Dirac integral of $\mathfrak{T}_{1/2}$ accounts for all transverse mode contributions (see [42] for analytical approximation for $\mathfrak{T}_{1/2}$, and also note that all quantities appearing as arguments of Fermi-Dirac integrals are normalized to k_BT). To obtain the total -2D electron density, we need to integrate Eq. (2.22b) over E_l . We also need to sum contributions from all the conduction band valleys and their subbands. Finally, we canget the 3D electron density by multiplying the corresponding distribution function $\left|\xi^n(z;x)\right|^2$ to the 2D density matrix at each longitudinal lattice node. The 3D electron density is fed back to the Poisson equation solver for the self-consistent solution.

Once self-consistency is achieved, the terminal current can be expressed as a function of the transmission coefficient [10]. The transmission coefficient from the source contact to the drain contact is defined in terms of the Green's function as [10]

$$T_{SD}(E) = trace\left[\Gamma_1(E)G(E)\Gamma_2(E)G^+(E)\right]$$
(2.23)

where *trace* means sum of the diagonal elements of a square matrix and Γ_1 [Γ_2] is the source (drain) broadening function defined above. It is straightforward to write the transmitted current as-

$$I(E_{l}) = \frac{q}{h} \int_{0}^{+\infty} \frac{2}{\pi \hbar} \sqrt{\frac{m_{y}^{*}}{2E_{k_{j}}}} [f(\mu_{s} - E_{l} - E_{k_{j}})A_{s} - f(\mu_{D} - E_{l} - E_{k_{j}})A_{D}]T_{SD}(E_{l}, E_{k_{j}}).dE_{k_{j}}}$$

$$(2.24a)$$

where the 2 in the numerator is for spin degeneracy. Note that T_{SD} is independent of transverse energy E_{k_j} and can therefore be moved out of the integration sign. Eq. (2.24a) then reduces to

$$I(E_{l}) = \frac{q}{-\hbar^{2}} \sqrt{\frac{m_{y}^{*} k_{B} T}{2\pi^{3}}} [\Im_{-1/2}(\mu_{S} - E_{l}) - \Im_{-1/2}(\mu_{D} - E_{l})] T_{SD}(E_{l})$$
(2.24b)

The total current is obtained from integrating over E_l and summing over all valleys and subbands.

2.1.6 Solution of 1D Schrödinger Equation

In this subsection we show the discretization of 1D Schrödinger equation using the finite element technique. Consider-the-1D-Schrödinger-equation---

$$\left[-\frac{\hbar^2}{2m^*}\frac{d^2}{dx^2} + E_C(x)\right]\psi(x) = E\psi(x)$$
(2.25)

where m^{\bullet} is the electron effective mass, $E_{C}(x)$ is the conduction band edge (with the confining potential-included) and *E*-is-the energy, with the boundary conditions

$$|\psi|_{x=0} = 0 \text{ and } |\psi|_{x=1} = 0$$
 (2.26)

(For open boundary conditions with self-energies, i.e., for Green's function see [40]). In FEM, Eq. (2.25) can be solved along with its associated boundary conditions by solving the equivalent variational-problem-defined by [44]

$$\begin{cases} \delta F(\psi) = 0 \\ \psi|_{x=0} = 0 \\ \psi|_{x=-L_{x}} = 0 \end{cases}$$
(2.27)

where

$$F(\psi) = \frac{1}{2} \int_{0}^{L_{x}} \left[\frac{\hbar^{2}}{2m^{*}} \left(\frac{d\psi}{dx} \right)^{2} + E_{C} \psi^{2} \right] dx - \frac{1}{2} E \int_{0}^{L_{L_{x}}} \psi^{2} dx$$
(2.28)

The different steps of the 1D_FEM are explained below:

(i) Discretization

This is the first step of FEM. Here we divide the solution domain $(0, L_X)$ into small subdomains, which in this case will be short line segments as shown in Fig. 2-3.

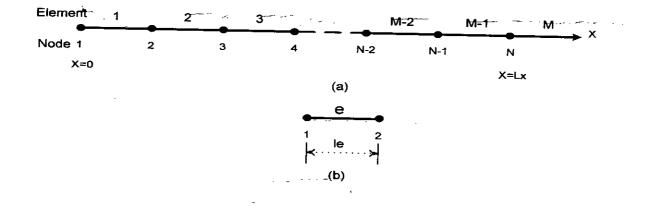


Fig. 2.3 One-dimensional domain subdivided into linear elements. (a) Element and global node numbers. (b) Linear element with local node numbers.

Let l^e (e=1,2,...,M) denote the length of the *e*th element *M*-denote the total number of elements and x_i (i=1,2,...,N) denote the position of the *i*th node with $x_1=0$ and $x_N=L_x$. In the following, the superscript *e*-is used to denote the quantity-with a local number as its subscript, while for all other quantities the subscript is a global number. In this simple 1D case, the local and global systems are related by

$$x_1^e = x_e$$
 and $x_2^e = x_{e+1}$ for $e=1,2,...,M$. (2.29)

(ii) Selection of interpolation function:

We select linear interpolation functions for simplicity because they are adequate for - -the-most cases. In this case, $\psi(x)$ within the *e*th element may be approximated as

$$\Psi^e(x) = a^e + b^e x \tag{2.30}$$

where a^e and b^e are constants to be determined. Enforcing Eq. (2.30) at the two ends of element e, we obtain

$$\psi^{e}(x) = \sum_{j=1}^{2} N_{j}^{e}(x) \psi_{j}^{e}$$
(2.31)

where N_1^e and N_2^e are the interpolation or basis functions given by $N_1^e(x) = \frac{\overline{x_2^e} - x}{l^e}$ and

$$N_2^e(\mathbf{x}) = \frac{\mathbf{x} - \mathbf{x}_1^e}{l^e}$$
 with $l^e = \mathbf{x}_2^e - \mathbf{x}_1^e$. Obviously, $N_j^e(\mathbf{x}_i^e) = \delta_{ij}$ as shown in Fig. 2.4.

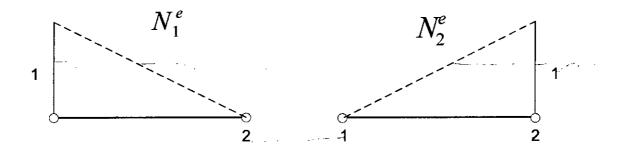


Fig. 2.4 One dimensional linear interpolation functions

(iii) Formulation of the system of equations

Derivation of Elemental Equitons-

For the formulation we use Ritz method. Assuming homogenous Neumann boundary condition for the time being Eq. (2.25) can be written as

$$F(\psi) = \sum_{e=1}^{M} F^{e}(\psi^{e})$$
(2.32)

where

$$F^{e}(\psi^{e}) = \frac{1}{2} \int_{x_{1}^{e}}^{x_{2}^{e}} \left[\frac{\hbar^{2}}{2m^{*}} \left(\frac{d\psi^{e}}{dx} \right)^{2} + E_{C}(\psi^{e})^{2} \right] dx - \frac{1}{2} E_{x_{1}^{e}}^{x_{2}^{e}} (\psi^{e})^{2} dx$$
(2.33)

Substituting Eq. (2.33) into Eq. (2.32) and then taking the derivative of F^e with respect to ψ^e , we obtain

$$\frac{\partial F^e}{\partial \psi_i^e} = \sum_{j=1}^2 \psi_j^e \int_1^{z_2} \left(\frac{\hbar^2}{2m_-^*} \frac{dN_i^e}{dx} \frac{dN_j^e}{dx} + E_C N_i^e N_j^e - E N_i^e N_j^e \right) dx$$
(2.34)

which in matrix form can be written as

?

$$\left\{\frac{\partial F^{e}}{\partial \psi^{e}}\right\} = \left[K^{e}\right]\left\{\psi^{e}\right\} = E\left[S^{e}\right]\left\{\psi^{e}\right\} = E\left[S^{e}\right]\left\{\psi^{e}$$

with

$$K_{ij}^{e} = \int_{x_{i}}^{x_{i}} \left(\frac{\hbar^{2}}{2m^{*}} \frac{dN_{i}^{e}}{dx} \frac{dN_{j}^{e}}{dx} + E_{c}N_{i}^{e}N_{j}^{e} \right) dx \qquad (2.36)$$

and

$$S_{ij}^{e} = \int_{x_{1}}^{x_{2}} \left(N_{i}^{e} N_{j}^{e} \right) dx$$
 (2.37)

We note that $[K^e]$ and $[S^e]$ are-symmetric and, if m^* and E_C are constant or can be approximated by constants within each element, their matrix elements can be evaluated analytically [44]. The result is

$$K_{11}^{e} = K_{22}^{e} = \frac{\hbar^{2}}{-l^{e}} + E_{c} \frac{l^{e}}{-3}$$
(2.38a).

$$K_{12}^{e} = K_{21}^{e} = -\frac{\frac{\hbar^{2}}{2m^{*}}}{l^{e}} + E_{C}\frac{l^{e}}{6}$$
(2.38b)

$$S_{11}^e = S_{22}^e = \frac{l^e}{3}$$
 and $S_{12}^e = S_{21}^e = \frac{l^e}{6}$ (2.39)

Assembly to Form the System-of Equations

We will not discuss the assembly procedure for 1D as it is similar to and yet simpler than the 2D case discussed in the next section for 2D FEM formulation of Poisson equation. After the assembly is complete, we get-

$$[K] = \sum_{e=1}^{M} \left[\overline{K^e}\right] \text{ and } [S] = \sum_{e=1}^{M} \left[\overline{S^e}\right]$$
(2.40)

where bar denotes that the corresponding matrix has been augmented by zero filling so that it is the same size as K or S.

Finally, standard procedures [44] can be used to find the eigenvalues and associated eigenfunctions.

2.2 Solution of 2D-Poisson-Equation

For self-consistency of the solution, Schrödinger equation has to be solved iteratively with the Poisson equation. In this section, we show solution technique for the 2D Poisson equation in the x-z plane shown in Fig. 2.1. The Poisson equation in this case is

$$-\frac{\partial}{\partial x}\left(\varepsilon_{x}\frac{\partial\phi}{\partial x}\right)-\frac{\partial}{\partial z}\left(\varepsilon_{z}\frac{\partial\phi}{\partial z}\right)=\frac{q}{\varepsilon_{0}}\left[n(x,z)-N_{D}(x,z)\right]$$
(2.41)

where $\phi(x,z)$ is the potential, n(x,z) is the electron density, $N_D(x,z)$ is the ionized donor density (complete ionization is assumed) at position (x,z), q is the magnitude of electronic charge, ε_0 is absolute permittivity, ε_x and ε_z are the relative permittivities in the x and z directions, respectively.

We can solve Eq. (2.41)-using FDM or FEM approach. However, when solving a coupled set of equations (the Poisson equation and transport equation), direct use of Eq. (2.41) leads to slow convergence [45]. Fortunately, there is a better solution algorithm for solving the Poisson equation [45]. This algorithm can provide more efficient convergence

in the iteration_loop of the Poisson and transport equation. This algorithm involves performing a variable change to n, namely expressing n in terms of the potential and a quasi-Fermi energy, F_n . The quasi-Fermi potential energy is computed based on the old potential as

$$F_n = -q\phi_{old} + k_B T \cdot \mathfrak{I}_{1/2}^{-1} \left(\frac{n}{N_C}\right)$$
(2.42)

where $\mathfrak{T}_{1/2}^{-1}$ stands for the inverse Fermi-Dirac integral of order 1/2 and N_{c} the effective density of states in the conduction band (a normalization factor). Analytical approximation for $\mathfrak{T}_{1/2}^{-1}$ can be found in [46].) The electron density term in Eq. (2.41) now becomes

$$n = N_C \mathfrak{I}_{1/2} \left[\frac{F_n + q\phi}{k_B T} \right]$$
(2.43)

With this variable change, Eq. (2.41) now represents a set of nonlinear equations for the potential. The advantage of this approach is that it builds a negative feedback into the iterative process-[45]. If the potential increases (conduction band decreases) during the Poisson solution, the subsequent transport solution will increase n(x,z) as carriers flow to regions of lower energy. This coupling is built into the Poisson equation when Eq. (2.42) is used. This approach has proven effective in previous quantum and semiclassical transport simulations [47-49] and proved similarly effective in this work.

We now discretize the Poisson equation using the FEM approach and for the time being we rewrite Eq. (2.41) as

$$-\frac{\partial}{\partial x}\left(\varepsilon_{x}\frac{\partial\phi}{\partial x}\right) - \frac{\partial}{\partial z}\left(\varepsilon_{z}\frac{\partial\phi}{\partial z}\right) = f \qquad (2.44)$$

with

$$f = \frac{q}{\varepsilon_0} [n(x,z) - N_D(x,z)]$$
(2.45)

and use homogeneous Neumann boundary condition. Real boundary conditions will be imposed after the assembly of the system of equations, as was done for the 1D Schrödinger equation. The variational problem equivalent to the boundary-value problem in Eq. (2.44) is given by [44]-

$$-\delta F(\phi) = 0 \qquad \qquad -2.46$$

where

$$F(\phi) = \frac{1}{2} \iint_{\Omega} \left[\varepsilon_x \left(\frac{\partial \phi}{\partial x} \right)^2 + \varepsilon_z \left(\frac{\partial \phi}{\partial z} \right)^2 \right] d\Omega - \iint_{\Omega} f \phi d\Omega$$
(2.47)

2.2.1 Domain Discretization

This is the first step in the FEM approach. The two-dimensional DG-MOSFET structure is subdivided into a number of small triangular elements as shown in Fig. 2.5. There exist other methods [44] for the discretization of 2D region using linear triangular elements, but they are not suited for this case because of our requirement that we need to solve the 1D Schrödinger-in the vertical slices (z-direction) and the NEGF in the x-direction. So the grid points in the in both the dimensions should on straight lines (grid spacing may non-uniform). The nodes and the triangular elements are numbered as shown in the figure. The discretization provides us with the following information:

```
Nn: number of (global) nodes
Ne: number of (triangular) elements
x : x-coordinate of the nodes (Ne x 1)
z : z-coordinate of the nodes (Ne x 1)
te: connectivity matrix (3 x Ne)
Nb: no. of boundary points (at the two gate contacts)
td: array of the global numbers of boundary nodes (Nb x 1)
```

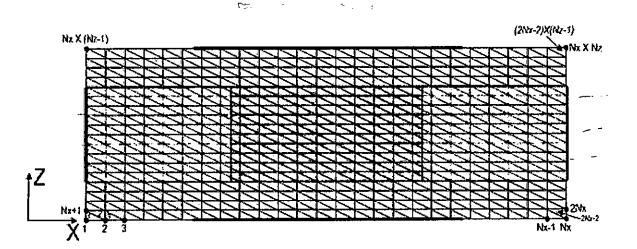


Fig.2.5 Domain discretization of the 2D DG MOSFET structure. Global node numbers and the element numbers are also shown.

All the quantities are self-explanatory, except *te*. Any node in the domain has a local number in the triangle it is a vertex of (and, of course, the global number). *te* connects a local number_to_its_associated_global_number._*te*_has Ne_rows,_each_for_a_triangle._In-a single row, a column contains the global number of the node with the column number as its local number._-------

2.2.2 Elemental Interpolation Function

1

After the discretization is complete, we need to approximate the unknown function ϕ within each element through the use of interpolation functions. For the linear triangular elements, the unknown function ϕ within each element is approximated as

$$\phi^{e}(x,z) = a^{e} + b^{e}x + c^{e}z$$
(2.48)

where a^{e}_{-} , b^{e}_{-} , and c^{e}_{-} are constant coefficients to be determined and e is the element number. The interpolation function for a node has a value of one at its associated node and it linearly goes to zero as we move towards the other two nodes, as shown in Fig. 2.6 below.

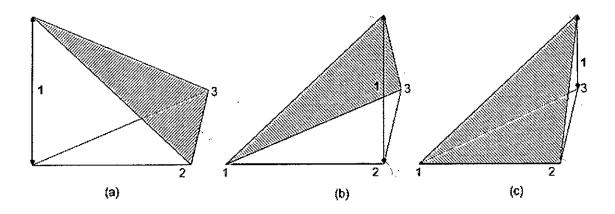


Fig. 2.6 Linear interpolation functions for a triangular element. (a) N_1^e , (b) N_2^e , (c) N_3^e . The planar surfaces of the functions are shaded.

Enforcing Eq. (2.48) at the three nodes, we obtain

$$\phi_1^e(x,z) = a^e + b^e x_1^e + c^e z_1^e$$

$$\phi_2^e(x,z) = a^e + b^e x_2^e + c^e z_2^e$$

$$\phi_3^e(x,z) = a^e + b^e x_3^e + c^e z_3^e$$

where ϕ_j^e is the value of ϕ^e at node *j* and x_j^e and z_j^e (*j*=1,2,3) denote the co-ordinate values of the *j*th node in the *e*th element. Solving for the constants a^e , b^e , and c^e in terms of ϕ_j^e , and substituting them back into Eq. (2.48) yields

$$\phi^{e}(x,z) = \sum_{m=j=1}^{3} N_{j}^{e}(x,z)\phi_{j}^{e}$$
(2.49)

where $N_j^e(x,z)$ are the interpolation or expansion functions given by

$$N_{j}^{e}(x,z) = \frac{1}{2\Delta^{e}} (a_{j}^{e} + b_{j}^{e}x + c_{j}^{e}z) \qquad j=1,2,3$$
(2.50)

in which

$$a_{1}^{e} = x_{2}^{e} z_{3}^{e} - z_{2}^{e} x_{3}^{e}; \qquad b_{1}^{e} = z_{2}^{e} - z_{3}^{e}; \qquad c_{1}^{e} = x_{3}^{e} - x_{2}^{e}$$

$$a_{2}^{e} = x_{3}^{e} z_{1}^{e} - z_{3}^{e} x_{1}^{e}; \qquad b_{2}^{e} = z_{3}^{e} - z_{1}^{e}; \qquad c_{2}^{e} = x_{1}^{e} - x_{3}^{e}$$

$$a_{3}^{e} = x_{1}^{e} z_{2}^{e} - z_{1}^{e} x_{2}^{e}; \qquad b_{3}^{e} = z_{1}^{e} - z_{2}^{e}; \qquad c_{3}^{e} = x_{2}^{e} - x_{1}^{e}$$

and

$$\Delta^{e} = \frac{1}{2} \begin{vmatrix} 1 & x_{1}^{e} & z_{1}^{e} \\ 1 & x_{2}^{e} & z_{2}^{e} \\ 1 & x_{3}^{e} & z_{3}^{e} \end{vmatrix} = \frac{1}{2} (b_{1}^{e} c_{2}^{e} - b_{2}^{e} c_{1}^{e})$$

= area of the eth-element.

It can be seen that the interpolation functions have the property

$$N_{i}^{e}(x_{j}^{e}, z_{j}^{e}) = \delta_{ij} = \begin{cases} 1 & i = j \\ 0 & j \neq -j \end{cases}$$
(2.51)

As result, at node *i*, ϕ^e reduces to its nodal value φ_i^e . Another point to note is that $N_j^e(x,z)$ vanishes when the observation point (x,z) is on the element side opposite to the *j*th node. Therefore, the value of ϕ^e at an element side is not related to the value of ϕ at the opposite node-but rather-it is determined by the values at the two end points of its associated side. This important feature guarantees the continuity of the solution across the element sides.

2.2.3 Formulation of the System of Equations

With the expansion of ϕ given in Eq. (2.49), we use Ritz method to formulate the system of equations. For simplicity, let us first consider the homogeneous Neumann -boundary condition. In this case, the functional can be written as -

$$F(\phi) = \sum_{e=1}^{M} F^{e}(\phi^{e})$$
(2.52)

1

where M denotes the total number of elements and F^{e} is the subfunctional given by

$$F^{e}(\phi^{e}) = \frac{1}{2} \iint_{\Omega^{e}} \left[\varepsilon_{x} \left(\frac{\partial \phi^{e}}{\partial x} \right)^{2} + \varepsilon_{z} \left(\frac{\partial \phi^{e}}{\partial z} \right)^{2} \right] d\Omega - \iint_{\Omega^{e}} f \phi^{e} d\Omega$$
(2.53)

with ϕ^e denoting the domain of the *e*th element. Introducing the expression in Eq. (2.49) for ϕ^e and differentiating F^e with respect to ϕ^e yields

$$\frac{\partial F^{e}}{\partial \phi^{e}_{i}} = \sum_{j=1}^{3} \phi^{e}_{j} \iint_{\mathcal{F}^{e}} \left(\varepsilon_{x} \frac{\partial N^{e}_{i}}{\partial x} \frac{\partial N^{e}_{j}}{\partial x} + \varepsilon_{z} \frac{\partial N^{e}_{i}}{\partial z} \frac{\partial N^{e}_{j}}{\partial z} \right) d\Omega - \iint_{\Omega^{e}} fN^{e}_{i} d\Omega \qquad i = 1, 2, 3$$
(2.54)

or

$$\left\{\frac{\partial F^{e}}{\partial \phi^{e}}\right\} = \left[K^{e}\right]\!\!\left\{\phi^{e}\right\} - \left\{b^{e}\right\}$$
(2.55)

where

$$\left\{\frac{\partial F^e}{\partial \phi^e}\right\} = \left[\frac{\partial F^e}{\partial \phi_1^e}, \frac{\partial F^e}{\partial \phi_2^e}, \frac{\partial F^e}{\partial \phi_3^e}\right]^T; \qquad \left\{\phi^e\right\} = \left[\phi_1^e, \phi_2^e, \phi_3^e\right]^T$$

The elements of the matrix $[K^{c}]$ are determined by

$$K_{ij}^{e} = \iint_{\mathbf{h}^{e}} \left(\varepsilon_{x} \frac{\partial N_{i}^{e}}{\partial x} \frac{\partial N_{j}^{e}}{\partial x} + \varepsilon_{z} \frac{\partial N_{i}^{e}}{\partial z} \frac{\partial N_{j}^{e}}{\partial z} \right) dx dz \qquad i, j = 1, 2, 3$$
(2.56)

- - while-the elements of the vector $\{b^e\}$ by

$$b_{i}^{e} = \iint_{\Omega^{e}} fN_{i}^{e} dx dz \qquad i = 1, 2, 3$$
(2.57)

It is evident that $[K^e]$ is a symmetric matrix. If ε_x , ε_z and f are constant within each element and equal to ε_x^e , ε_z^e , and f^e , respectively, then

$$K_{ij}^{e} = \frac{1}{4\Delta^{e}} \left(\varepsilon_{x}^{e} \overline{b}_{i}^{e} \overline{b}_{j}^{e} + \overline{\varepsilon}_{z}^{e} \overline{c}_{i}^{e} \overline{c}_{j}^{e} \right)$$
(2.58)

$$b_i^e = \frac{\Delta^e}{3} f^e \tag{2.59}$$

2.2.4 Assembly to form the System of Equations

With the elemental Eq. (2.53), we can assemble all *M* elements, and then impose the stationarity requirement on *F*-to-find the system of equations-

$$\left\{\frac{\partial F}{\partial \phi}\right\} = \sum_{e=1}^{M} \left\{\frac{\overline{\partial F^{e}}}{\partial \phi^{e}}\right\} = \sum_{e=1}^{M} \left(\left[\overline{K^{e}}\right] \left\{\overline{\phi^{e}}\right\} - \left\{\overline{b^{e}}\right\}\right) = \{0\}$$
(2.60)

which can be written compactly as-

$$-[K]\{\phi\} = \{b\} - -(2.61)$$

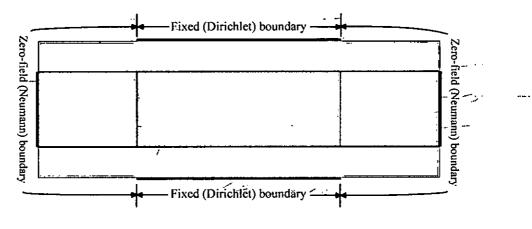
where [K] is assembled from $[\overline{K^e}]$, and similarly, $\{b\}$ is assembled from $\{\overline{b^e}\}$:

$$[K] = \sum_{e=1}^{M} \left[\overline{K^e}\right], \qquad \{b\} = \sum_{e=1}^{M} \left\{\overline{b^e}\right\}.$$
(2.62)

Given the connectivity matrix; te, we need to add K_{ij}^e to $K_{ie(i,e), ie(j,e)}$ and b_i^e to $b_{ie(i,e)}$ to find the augmented parameters.

2.2.5 Imposition of Actual Boundary-Condition

Up to this point, we assumed Neumann or zero field boundaries on all the sides of the simulated 2D surface. Now before the system of equations is ready to solve, we need to impose_the_actual boundary_conditions of the_device. The only_modification_needed_for this is to impose the Dirichlet boundary_condition on the Nb boundary_points at the two gate contacts. The boundary conditions are shown in Fig. 2.7.



... Fig. 2.7 Actual-boundary-conditions

The reason for using the zero field boundary condition instead of the usually-used Dirichlet boundary condition on the source contact and drain contact is given in [49] where it is shown that in the ballistic simulation if Dirichlet boundary condition is used in these two contacts then the electron density profile becomes sharply peaked at the two ends. To ensure a smooth density at the source and drain contacts the zero field boundary are more appropriate [49]. To impose the Dirichlet boundary condition at the two gate contacts_we-set

$$b_{id(i)} = p(i)$$
, $K_{id(i),id(i)} = 1$, $K_{id(i),j} = 0$ for $j \neq id(i)$ (2.63a)

and

$$b_j \leftarrow b_j - K_{j,id(i)}p(i), \qquad K_{j,id(i)} = 0 \quad \text{for } j \neq td(i)$$
 (2.63b)

for i = 1, 2, 3, ..., Nd.

2.2.6 Solution of the System of Equations

After the actual boundary conditions have been imposed Eq. (2.61) is ready to be solved. Standard routines [44] are available for solution of Eq. (2.61).

CHAPTER 3

Results and Discussions.

We presented the self-consistent simulation method using FUMS approach in Chapter 2. The results of the calculation for silicon symmetric double-gate nMOSFETs along with relevant discussions are presented in this Chapter. We assume <100> – orientation for silicon and, as stated in the previous Chapter, effective mass approximation has been considered valid. Effective mass approximation has been found accurate in describing the quantization effects of electrons in a MOS inversion layer [50]. The values of different parameters used in the calculation are given in Table 3.1.

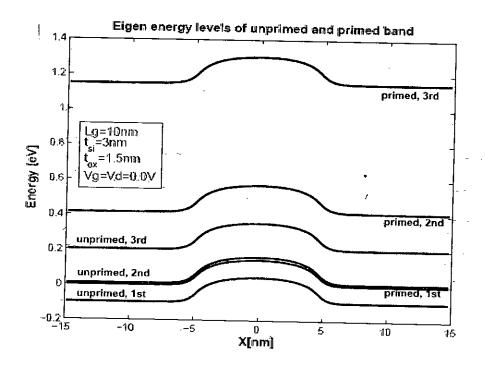
- Parameter -	Value
Electronic charge, q	1.602 x 10 ⁻¹⁹ C
Planck constant, h	6.626 x 10 ⁻³⁴ J-s
Reduced Planck constant, \hbar	1.055 x 10 ⁻³⁴ J-s
Free electron mass, m	9.11 x 10 ⁻³¹ kg
Longitudinal effective mass, m_l	0.91
Transverse effective mass, m_t	0.19
Oxide effective mass, m_{ox}	0.5
Permittivity of free space, ε_0	8.854 x 10 ⁻¹² F/m
Relative permittivity of silicon,	11.7
E _{si}	
Relative permittivity of silicon	_3.9
dioxide, ε_{ox}	
Metal workfunction, w _m	4.25 eV
Work function difference between	3.34 eV
silicon and oxide, χ	
Boltzmann constant, k	1.38 x 10 ⁻²³ J/K
Effective density of states at	$2.8 \text{ x} 10^{25} \text{ m}^{-3}$
conduction band_edge, N_C	

Table 3.1: Important physical parameters values used in the simulation

3.1 Eigen-Energies and Eigenfunctions

The schematic diagram of the simulated device is as that is shown in Fig. 2.1. Due the confinement, the carriers are quantized in the vertical direction and subbands are formed at different energies. In this section, the various eigen energies and the corresponding eigen functions are shown at different conditions. Unless specified otherwise, all-the-simulations have been done at room temperature (300-K)-with n-type source and drain regions doped at 10^{20} cm⁻³. The channel is intrinsic, and the source/drain-channel junction is abrupt. No-gate-to-S/D-overlap-is-assumed. The-oxide thickness is 1.5 nm for both top and bottom gates.

Fig. 3.1 shows the subband energies at different vertical slices of the device in the transport or x-direction (Vg=Vd=0.0V). Electron penetration into the oxide is necessary in the nanoscale thickness levels involved and has duly been considered in the calculations, although it is trivial to do the simulation without considering—the penetration.



Fig; 3:1 Eigen energy levels along the transport direction (unbiased condition)

As expected at this unbiased condition (Vg=Vd=0.0V), the eigen energy_levels_are symmetric about the middle of the transport direction, and the are higher in the undoped silicon body region. The different simulation parameters are given-inside the figure. It_____ evident from the figure that the unprimed energy level (without symbol) is at a lower energy compared to the corresponding_primed_level. The reason_is_that_the_unprimed valley has higher effective mass in the density of states. As the level number increases so does the energy involved. Now the lower the energy involved-the higher the carrier population so higher band will have lower electron concentration. In Fig. 3.1, the third primed energy level is_at_very high_energy_levels_compared_to_the_other_lower-levels so that it will contain negligible carrier population compared to the other lower levels and can thus be totally disregarded without affecting the accuracy to any perceptible level but simulation time will be lower.

We will now consider the eigen_energy_levels_at_a_biased_condition: Vg=Vd=0.3V, which is typical of the device considered in this work. Note that the energy levels are considerably constant in the source (x=-5.0 nm-to-15.0 nm) and the drain (x= 5.0 nm to - 15.0 nm) regions because the electron concentrations are flat in these regions. As we progress along the channel eigen energy levels fall down until they are flat in the drain region. The source to-channel barrier is lower due to Vg and due to-Vd the bands bend down as we move towards drain.

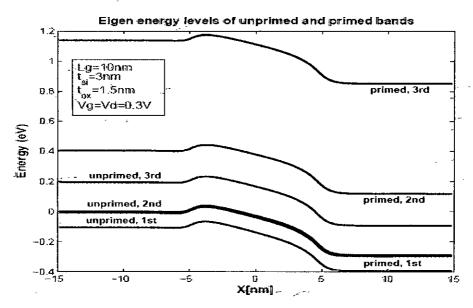


Fig. 3.2 Eigen energy levels along the transport direction (biased condition). From source to drain the eigen energy levels fall although there is an increase near the source-channel contact.

Let us now concentrate on_the (unprimed)_ground_state eigen_energy_along_the channel. Fig. 3.3 shows variation of the ground eigen state along the channel with Vg at a fixed Vd. For a particular Vd, as Vg increases it lowers down the barrier in the channel region so the eigen energy decreases but the eigen energy in the source and drain region remains constant irrespective of the value of Vd.

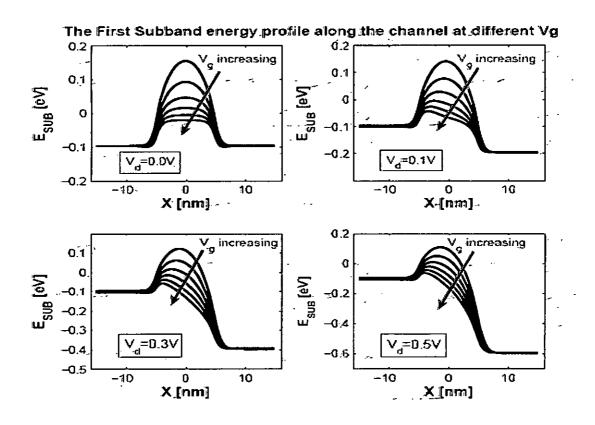


Fig. 3.3 The first (groundstate) eigen energy variation along the channel with Vg. Vd is (clock vise from top left corner) 0.0V, 0.1V, 0.3V, and 0.5V

Fig. 3.4_shows variation of the ground eigen_state_along_the_channel_with -Vd-at a fixed Vg. For a particular Vg, as Vd increases it lowers down the barrier in the drain side of channel so the eigen energy decreases in this region. Also clear from the figure is that the eigen energy decreases in the drain region as drain voltage pulls down Ec in drain region but the eigen energy in the source-remains constant irrespective of the value of Vd.

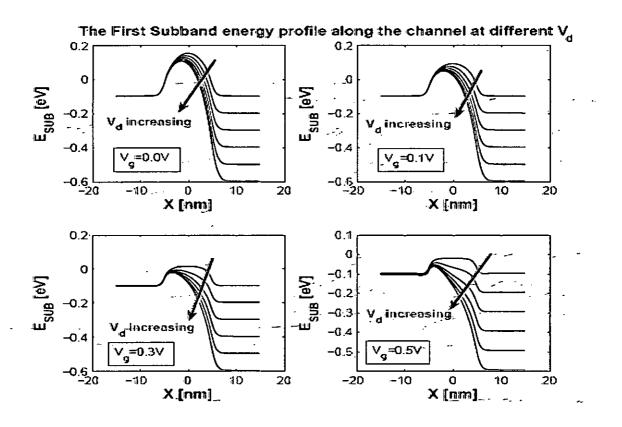


Fig. 3.4 The first (groundstate) eigen energy variation along the channel with Vd. Vg is (clock wise from top left corner) 0.0V, 0.1V, 0.3V, and 0.5V

The squared eigen functions or wavefunctions along a vertical slice (y-direction) at the middle if the channel (x=0) of the device at Vd=Vg=0.0V are shown in Fig. 3.5. The area under_any_function is_1, but_the_unprimed wavefunctions-have-higher-maxima. Due to the silicon-to-oxide energy barrier, the eigen functions are zero in the oxide regions except near the oxide-silicon-interface where because of penetration of electron the wavefunctions are nonzero. As stated earlier, the incorporation of penetration effect in the calculation is_necessary_for the_thickness_of_oxide-involved. We can also clearly-see that the penetration effect for primed valley is higher due to the lower density of state mass. Fig 3.6 shows the same thing except that in this case $Vg=Vd=0.3V_{t}$. As can be seen from data cursors of the two figures there is a slight drop in amplitude of different wavefunctions as the gate and drain voltage is increased.

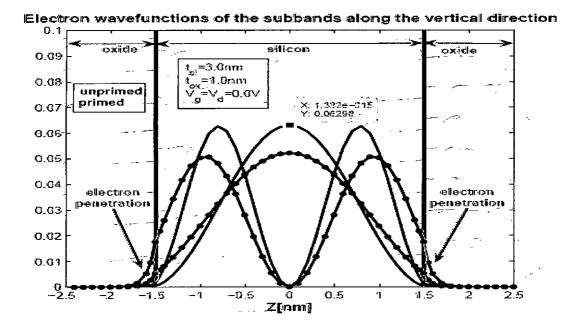


Fig. 3.5 The unprimed and primed eigen functions along z-direction at the middle of the channel. Curves without symbol are for unprimed valley while those with symbol are for primed valleys. Vg=Vd=0.0V.

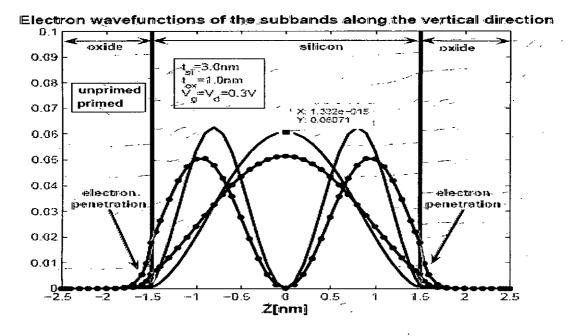


Fig. 3.6 The unprimed and primed eigen functions along z-direction at the middle of the channel. Curves without symbol are for unprimed valley while those with symbol are for primed valleys. Vg=Vd=0.3V.

3.2 Electron-Density-Profile

In this section, we present simulation results for electron-density-in-the device for different stated conditions. In Fig. 3.7, 2D electron density of different subbands along the channel-is shown. In this unbiased condition, the density-profile should-be-symmetric about the middle of the channel and this is supported by Fig. 3.11. If we consider the comparative population, then contribution of three low concentration bands may be neglected in the self-consistent calculation.

For the typical biased condition (Vg=Vd=0.5V), the concentration profile is plotted in Fig. 3.8. Similar to the unbiased case, the contribution of three low concentration bands may be neglected in this condition too. It is expected that the density profile willnot be symmetrical, but concentration at the drain side of the channel is lower due to the presence of a source to drain voltage. This is a direct result of the curves in Fig. 3.3 and Fig. 3.4.

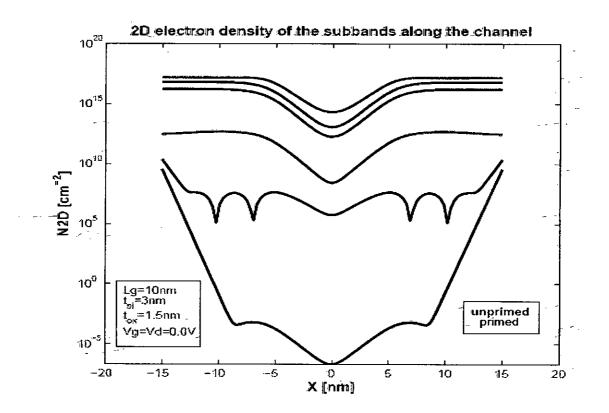


Fig. 3.7 2D electron density of different subbands along the channel (unbiased condition)

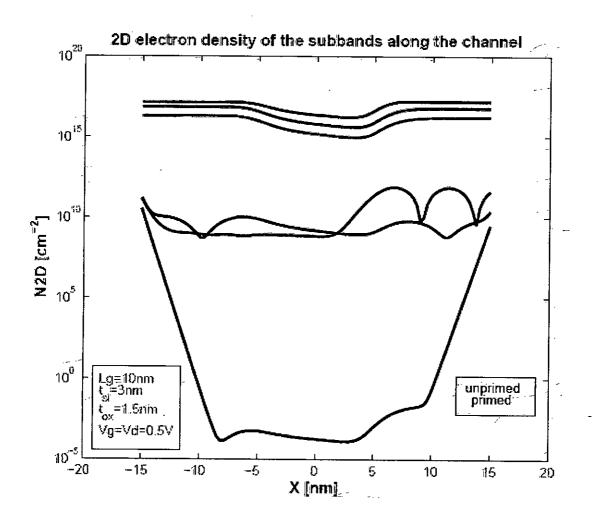


Fig. 3.8 2D electron density of different subbands along the channel (biased condition)

Then Fig. 3.9 shows variation of 2D electron concentration with Vg at two different drain voltages; upper part (Vd=0.0V) and lower part (0.5V). As Vg increases, the electron density in the channel increases. In the case of Vd=0.5V, due to lower channel to gate voltage, concentration is lower in the drain side compared to the source side.

Next variation of 2D charge density with drain voltage is shown for the cases of Vg= 0.0V and Vg=0.5V is depicted in Fig. 3.10. As Vd increases, concentration at the drain side of the channel decreases, but concentration at the source remains constant. For higher Vg concentration in the channel is higher as can be seen from a comparison of the upper (Vg=0.0V) and lower (Vg=0.5V) part of Fig. 3.10.

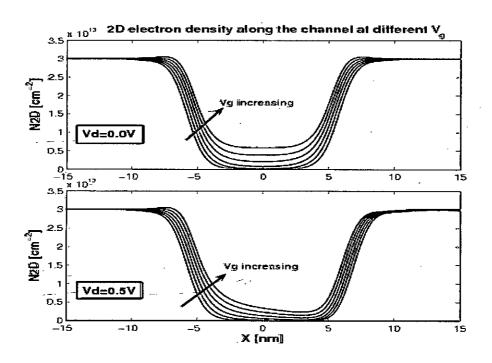


Fig. 3.9 2D electron density variation along the channel with Vg. Upper_part for Vd=0.0V-and lower part for Vd=0.5V

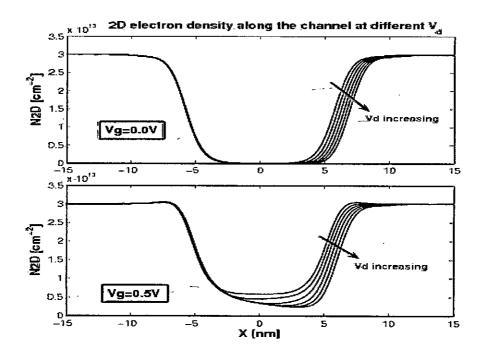


Fig. 3.10 2D electron density variation along the channel with Vd. Upper part for Vg=0.0V and lower part for Vg=0.5V

Finally the 3D electron density variation-along with the ground-state wavefunction is shown below for three different device thicknesses in Fig. 3.11. Here, Vg=Vd=0.5V. Fig. 3.11(a) shows 3D for $t_{si}=1.5$ nm while Eig. 3.11(b) shows the same for $t_{si}=3.0$ nm. As the thickness increases the density maximum decreases. If increase in t_{si} in continued then the 3D density profile develops a valley in the source and drain regions as depicted in Fig. 3.16(c). Thus at sufficiently large t_{si} there will be two independent channel close to the two oxide-silicon interfaces. But as the thickness is reduced the valley vanishes as the two channels combine.

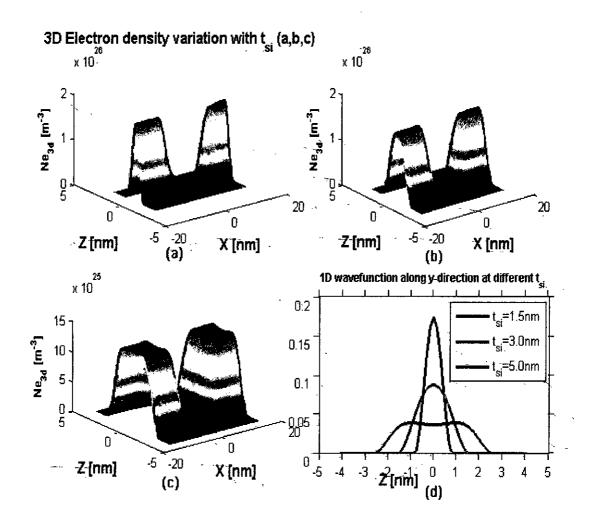


Fig. 3.11 Variation of 3D electron density with silicon thickness, t_{si} (a), (b), and (c) show the 3Ddensity for t_{si} values 1.5, 3.0, and 5.0 nm, respectively; while (d) shows the ground state eigenfunction along a vertical slice in the middle of the source region in the x-direction.

<u>-</u> م

3.3 3D Conduction Band Edge Profile

In this section the 3D conduction band edge (E_C) profile is pictured for different biasing conditions and silicon thickness. Fig. 3.12 plots the 3D and 1D conduction band edge in the device with silicon thickness t_{si} =3.0 nm. Note that the barrier from silicon to oxide is not considered when plotting the data. Fig. 3.12(a) shows the profile for Vg=0.3V, Vd=0.0V while Fig. 3.12(b) shows the profile at Vg=Vd=0.3V. The selfconsistent E_C profile in (a) is symmetric along the lines x=0 and y=0. E_C is higher at the channel region as the silicon in this region is undoped while it is lower_in_the doped source and drain regions. For the Vd=0.3V case (b), when moving from source towards the drain through the channel, a small barrier is encountered at the source and channel junction (electrons from the source are reflected by this small barrier). After this barrier, E_C decreases due to Vd. Clearly, electrons from drain face a very high barrier towards the channel and are mostly reflected back. The conduction band is almost flat in the two oxide regions. (c) and (d) show z-directed 1D E_C for conditions (a) and (b), respectively. In the silicon film E_C changes shape very slowly as assumed for UMS/FUMS approach.

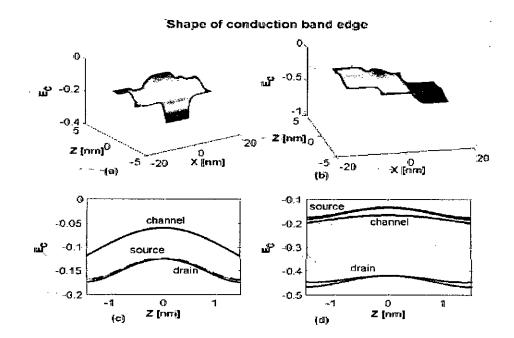


Fig. 3.12 Conduction band profile; (a) 3D, Vg=0.3V, Vd=0V, (b) 3D, Vg=0.3V, Vd=0.3V, (c) 1D, Vg=0.3V, Vd=0V, (d) 1D, Vg=0.3V, Vd=0.3V

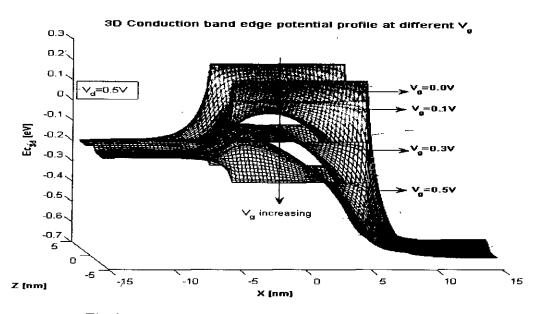


Fig. 3.13 3D conduction band profile at different Vg. Vd=0.5V

3D-Conduction band edge potential profile at different V_{d}

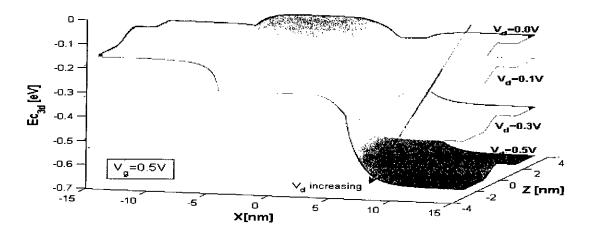


Fig. 3.14 3D conduction band profile at different Vd. Vg=0.5V

Variation of E_C with Vg and Vd are shown in Fig. 3.13 and Fig. 3.14, respectively. As Vg in increased with Vd fixed (0.5V), E_C is almost constant in the source and drain region, but it-drops down in the channel region (Fig. 3.13). As shown in Fig. 3.14, with increase in Vd for a constant Vg (0.5V), E_C drops down in the drain region (due to increasing Vd); but it is constant in the source and the source side of the channel.

3.4 Drain Current

The transport characteristic of the double gate (DG) MOSFET is discussed in this section. The general I_{DS} - V_{DS} characteristic of a DG MOSFET with L_g =10.0 nm and t_{si} =3.0 nm-is shown-in-the Fig. 3.15. As the drain-voltage increases for a fixed-gate voltage the drain to source current increases. For lower V_{DS} the increase in high, but as V_{DS} is increased above 0.2-0.3V the current starts to saturate. For a certain- V_{DS} value the current is higher for larger V_{GS} .

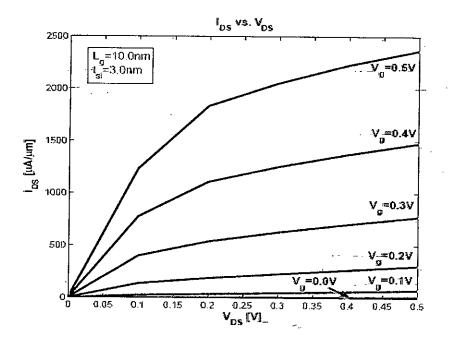


Fig. 3.15 I_{DS} - V_{DS} characteristic of a DG MOSFET with L_g =10.0 nm and t_{si} =3.0 nm. Current values are per_1- μ m width of the device.

The second figure in this section (Fig. 3.16) illustrates the effect of silicon thickness on the l_{DS} - V_{DS} characteristics. As the silicon thickness is increased the drain current increases for fixed V_{DS} and V_{GS} . As t_{si} is increased the channel area increases because the whole of the silicon acts as channel, so larger thickness gives more current. Of course, this is only true for thickness level less than 5 nm. Beyond 5 nm two distinct channel develops and UMS/FUMS is not applicable for $t_{si} > 5$ nm.

×.

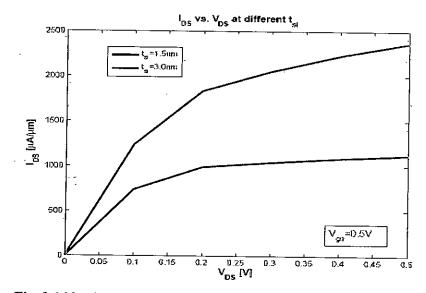


Fig-3.16 l_{DS} (per μ m width) variation with silicon thickness (t_{si}). V_{GS}=0.5V

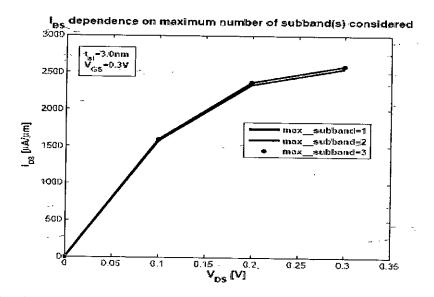


Fig. 3.17 I_{DS} (per μ m width) vs. V_{DS} characteristic dependence on maximum number of subbands considered in the calculation

Above the effect of the number of subbands considered on the drain current is shown in Fig. 3.17. When 2 or 3 subbands are considered there is essentially no difference in the drain current and a single subband also gives-current values-close to when 2 or 3 subbands are considered. Usually 2 subbands are sufficient for the thicknesses involved in this thesis.

3.5 Validity of FUMS approach

In this section we demonstrate the validity of our FUMS approach by comparing its results with those of UMS approach. Fig. 3.18 shows comparison of subband levels and their populations produced by FUMS and UMS approach. The two approaches produce essentially the same results. But the advantage of the FUMS approach is that it requires much less time. Similar result is expected for I_{DS} versus V_{DS} characteristics as is supported by Fig. 3.19.

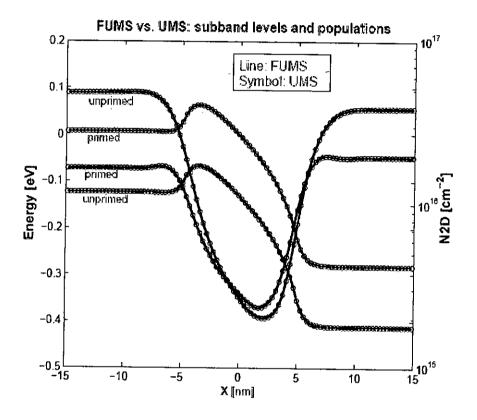


Fig. 3.18 Comparison of subband levels and their populations produced by FUMS and UMS approach. Solid line represents results of FUMS and symbol represents results of UMS.

Thus extensive simulation necessary to exploring novel devices can be made practical by our FUMS approach with FEM taking the responsibility of handling complex geometry.

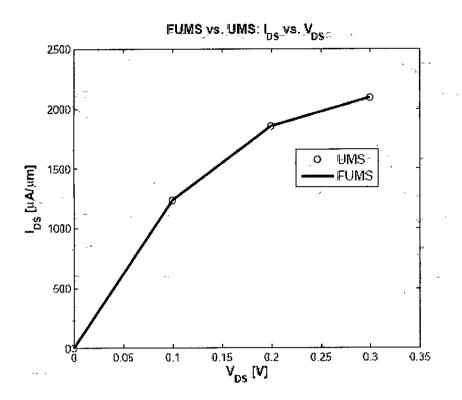
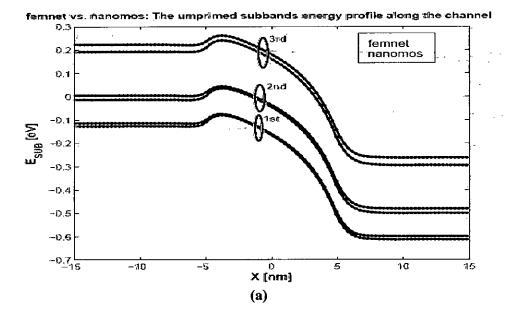


Fig. 3.19 Comparison of I_{DS} (per μ m width) vs. V_{DS} profiles produced by FUMS and UMS approach. Solid line represents results of FUMS and symbol represents results of UMS.

3.6 Comparison between femnet and nanomos simulation-results

In this section we show the validity of our simulation method by comparing the simulation outcomes of this work, called **femnet** (finite element method nanoelectronic transistor simulator), with similar results of the famous **nanomos** [45] in the nanohub [51]. The theory behind the two simulators is similar but not exactly; **nanomos** uses UMS approach for solving the 1D Schrödinger equation in the vertical direction (z-direction) while femnet uses the novel FUMS approach to reduce the time required to solve the 1D Schrödinger equation. Of course, the two simulators differ greatly in their PDE discretization; **nanomos** uses the simple FDM approach and femnet uses the more appropriate FEM approach. FEM allows easy handling of complex geometry and we can concentrate on a region where any variable can change rapidly by using dense mesh there. Still, the simulators produce similar results-as-shown-in-Figs. 3.20-3.22.



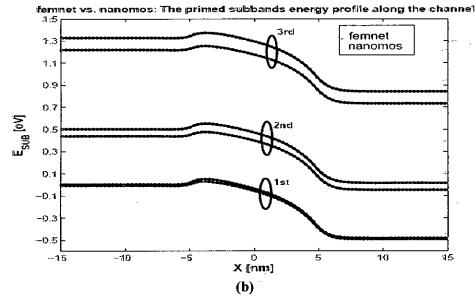
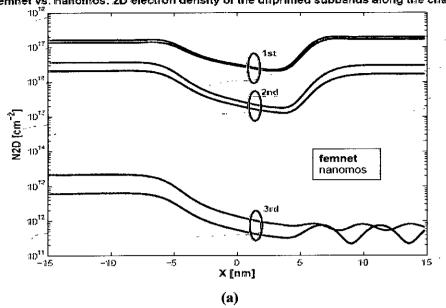


Fig. 3.20 Comparison between eigen energy levels produced by femnet (blue) and nanomos (red). (a) unprimed subbands, (b) primed subbands.

Fig. 3.20 plots the different subband energy levels in the DGMOSFET. The lower levels are very close but the higher levels have greater deviations. For femnet, the source to channel barrier is smaller so greater drain current is expected from FEM simulation.



femnet vs. nanomos: 2D electron density of the unprimed subbands along the channel

femnet vs. nanomos: 2D electron density of the primed subbands along the channel

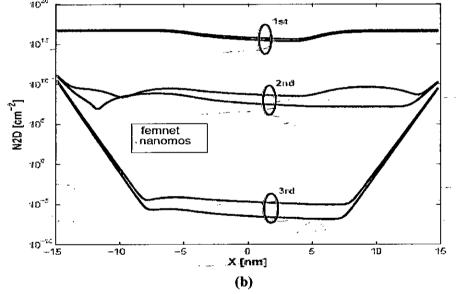


Fig. 3.21 Comparison between 2D subband populations produced by femnet (blue) and nanomos (red). (a) unprimed subbands, (b) primed subbands.

Here in Fig. 3.21, we plot the subband population of different subbands both for femnet and **nanomos**. The lowest subband population is very close but as the energy increases the difference becomes larger, still general trend is similar.

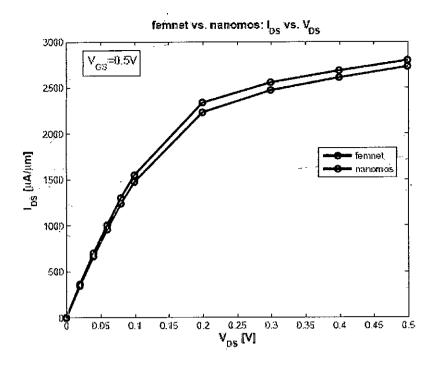


Fig. 3.22 Comparison between drain currents produced by femnet (solid filled circle in blue) and **nanomos** (solid open circle in red)

We compare the I_{DS} -vs. V_{DS} -characteristic for the two simulation approach in Fig. 3.22. As stated earlier, femnet shows larger currents for same V_{GS} although the general behavior is again confirmed to be in agreement.

CHAPTER 4

Conclusion

A modified and computationally efficient simulation approach for DG MOSFETs was presented in the previous Chapter. This Chapter summarizes the work along with directions for further study.

4.1 Summary

This thesis addressed device physics, modeling and design issues of nanoscale double gate MOSFETs at the quantum levels. A double gate MOSFET device with extremely scaled channellengths (less than 20 nm) and body thicknesses (less than 5 nm) was considered in this study. To accomplish the objectives, a simulation tool was prepared using MATLAB. The fundamental physics equations that were solved include the Poisson equation, which dictates the electrostatics in the devices, and the Schrödinger equation, which describes the transport and distribution of carriers in the devices.

In Chapter_3, we_prepared_a_2D_simulator,_termed_femnet,_for nanoscale_double-gate MOSFETs. The program solves open-boundary transport problems using non-equilibrium Green's function (NEGF) formalism using fast-uncoupled mode-space (FUMS) approach._Both_NEGF and FEM were employed in the simulation of nanoscale DG MOSFETs. The ballistic DG _MOSFETs simulation results of this_work were_compared with results of a standard simulation tool [45] and the appropriateness of our method and hence the use of FEM was justified. Due to the use of FUMS approach the simulation process is speeded up, but results are essentially similar to those obtained from the use of UMS approach.

Important_conclusions are: i)_MOSFETs_essentially_operate_as_classical_devices_until-the channel length shrinks below 10 nm, when quantum tunneling through the channel barrier becomes significant, limiting device scaling, ii) solving the Green's function in a mode space representation can greatly reduce the size of the problem and provides good accuracy as

51

compared to full 2D spatial discretization, iii) fast uncoupled mode space approach with FEM makes the method computationally affordable at the PC level.

4.2 Future Work

Further extensions in this study may include:

- 1) The NEGF approach can be used to simulate gate oxide leakage characteristics of 1D MOS structures. Currently, the widely used method in modeling gate leakage is the WKB approximation [52-53]. In this method quantum transmissions through the oxide layers are evaluated approximately in a post-process operation. The electrostatic profile is obtained separately, without considering the effects on the charge distribution of the tunneling current. In addition, incident electrons are represented by plane-waves, so the effects of 2D carriers in the inversion-layer on the gate tunneling may not be properly assessed. The NEGF method, however, can exactly solve the transport problem, when coupled to the Poisson equation, providing a way of self-consistently assessing the gate leakage and electrostatics profile. This method also allows us to examine the energy spectrum of the leakage current, which distinguishes the contributions from the 2D discrete states and 3D continuous states.
- 2) Different scattering model are available and scatter can be included in the simulation as it will give the method a real life touch. One method to study the dissipative transport can be the use of the Büttiker probe based scattering models where scattering centers are treated as reservoirs that change the energy or momentum of the carriers and not the total number of carriers in the system. It has been used in the nanoMOS [45], and recently, in a nanowire simulation [54]. Each scattering center is modeled through a perturbation strength characterized by a position dependent self-energy, which can be mapped onto an equivalent mebility.
- 3) Strained silicon MOSFETs are being studied in recent years. The methods presented in this work can be extended for the study of both uniaxially and biaxially strained DG MOSFETs.

In this case, the simple effective mass approximation might not be true because the electron and hole effective masses change with strain as does the band alignment.

The NEGF approach is a very powerful mathematical tool for addressing how a quantumstate evolutes temporally under a varieties of interactions within any tiny system (or quantum level device). As device scaling_continues, novel_structures/designs must eventually-take-over-the role currently being played by semiconductor-based transistors. Some recent works have brought carbon-tube and molecule-cluster_based_device_structures_into-focus-[55-58]. These new_areas_... provide us plenty of opportunities to apply the NEGF approach at theoretical research levels. In principle, the_NEGF_formalism_not_only enables_us_to_understand_the_microscopic_phenomena, but also enable us to exploit the hidden potential. Moreover, it should be noted that this approach is applicable in all non-equilibrium systems beyond electron devices [9, 59].

Bibliography

- [1]. G.E. Moore, "Progress-in-digital-integrated electronics," IEDM-Tech. Digest, pp. 11-13, _____ 1975.
- [2]. International Technology Roadmap for Semiconductors, Semiconductor Industry Association, 2005.
- [3]. Ý. Täur, D. Buchanan, W. Chen, D. Frank, K. Ismail, S.-H. Lo, G. Sai-Halasz, R. Viswanathan, H.-J. C. Wann, S. Wind and H.-S. Wong, "CMOS scaling into the nanometer regime," *Proc. IEEE*, 85, pp. 468-504, 1997.
- [4]. H.-S. Wong, D. Frank and P. Solomon, "Device Design Considerations for Double-Gate, Ground-Plane, and Single-Gated -Ultra-Thin-SOI-MOSFET's-at-the-25-nm-Channel_____ Length Generation," *IEDM Tech. Digest*, pp. 407-410, 1998.
- [6]. K. Banoo, Direct Solution of the Boltzmann Transport Equation in Nanoscale Si Device, Ph.D. dissertation, Purdue University, West Lafayette, IN, 2000.
- [7]. D.K. Ferry, R. Akis, D. Vasileska, "Quantum effects in MOSFETs: use of an effective potential in 3D-Monte Carlo-simulation of ultra-short-channel devices," *IEDM_Tech.__ Digest*, pp. 287 -290, 2000.
- [8]. L.V. Keldysh, "Quantum transport_equations_for_high electric_fields,"-Sov.-Phys.-JETP
 20, pp. 1018, 1965.
- -[9]- J. Rammer-and H. Smith, "Quantum-field-theoretical-method-in-transport-theory ofmetals," *Rev. Mod. Phys.* 62, pp. 323-359, 1986.
- [10]. S. Datta, *Electronic Transport_in_Mesoscopic Systems*, Cambridge University Press,
 Cambridge, UK, 1997.
- [11]. B. Davari, R.H. Dennard and G.G. Shahidi, "CMOS scaling for highperformance and low-power-the next ten years," Proc. IEEE, 89, pp. 595-606, 1995.
- [12]. H.-S. Wong, D. Frank and P. Solomon, C. H.-J. Wann and J. Welser, "Nanoscale CMOS," Proc. IEEE, 87, pp. 537-570, 1999.

- [13]. A.A. Abrikosov, L.P. Gorkov and I.E. Dzyaloshinski, *Quantum Field Theoretical* Methods in Statistical Physics, 2nd ed., Pergamon, New York, 1965.
- [14]. D.J. Frank, Y. Taur and H.-S. P. Wong, "Generalized Scale Length for Two-Dimensional Effects in MOSFETs," *IEEE Electron Dev. Lett.*, **10**, pp. 385-387, 1998.
- [15]. Y. Taur and T. Ning, *Fundamentals of VLSI_Devices*, Cambridge University Press, Cambridge, UK, 1998.
- [16]. Y.-C. Sun, Y. Taur, R.H. Dennard-and-S.P. Klepner, "Submicron-Channel-CMOS_for_____ Low-Temperature Operation," *IEEE Trans. Electron Devices*, 34, pp. 19-27, 1987.
- [17]. S.J. Wind, D.J. Frank, and H.-S. Wong, "Scaling silicon MOS devices to their limits," *Microelectronic Engineering*, **32**, pp. 271-282, 1996.
- [18]. Y. Taur, C.H. Wann and D. Frank, "25 nm-CMOS Design Considerations," IEDM Tech. -Digest, pp. 789-792, 1998.
- [19]. A. Wei, M.J. Sherony and D.A. Antoniadis, "Effect of Floating-Body Charge-on-SOI MOSFET Design," *IEEE Trans. Electron Dev.*, 45, pp. 430-438, 1998.
- [20]. L.T. Su, J.B. Jacobs, J.E. Chung and D.A. Antoniadis, "Deep-Submicrometer Channel Design in Silicon-on-Insulator (SOI) MOSFET's," *IEDM Tech. Digest*, pp. 183-186, 1994.
- [21]. Ren, S. Bourland, S. Lee, J. Denton, M.S. Lundstrom and R. Bashir, "Ultra-thin Body SOI by Controlled Oxidation of Thin Si-Membranes," presented at IEEE Silicon-Nanoelectronics Workshop, Honolulu, Hawaii, June 11-12, 2000.
- [22]. L. Chang, S. Tang, T. King, J. Bokor and C. Hu, "Gate Length Scaling and Threshold Voltage Control of Double-Gate MOSFETs," *IEDM Tech. Digest*, pp. 719-722, 2000.
- [23]. G.-Neudeck, T.-C. Su_and J. Denton, "Novel-Silicon Epitaxy for Advanced-MOSFET_____ Devices," *IEDM Tech. Digest*, pp. 169-172, 2000.
- [24]. M.-K Ieong, E.C. Jones, T. Kanarsky, Z. Ren, O. Dokumaci, R.A.-Roy, L. Shi, T. Furukawa, Y. Taur, R.J. Miller, H-S Wong, "Deep-Submicrometer Channel Design in Silicon-on-Insulator (SOI)-MOSFET's," to appear-in-IEDM, 2001.
- [25]. D.J. Frank, S. Laux and M. Fischetti, "Monte Carlo Simulation of a 30 nm Dual-Gate MOSFET: How Short Can Si Go?" *IEDM Tech. Digest*, pp. 553-556, 1992.

- [26]. Z. Ren, R. Venugopal, S. Datta and M.S. Lundstrom, "Examination_of_Design and Manufacturing Issues in a 10 nm Double Gate MOSFET using Nonequilibrium Green's Function Simulation," in IEDM Tech. Digest, pp. 107-110, 2001.
- [27]. M.S. Lundstrom, Fundamentals of Carrier Transport, 2nd ed., Cambridge University Press, Cambridge, UK, 2000.
- [28]. F. Assad, Z. Ren, D. Vasileska, S. Datta, and M.S. Lundstrom, "On the performance limits for Si MOSFET's: A theoretical study," *IEEE Trans. Electron Dev.*, 47, pp. 232-240, 2000.
- [29]. Y. Taur_et_al., "An analytical solution to a double-gate MOSFET_with_undoped_body,"
 IEEE Electron Device Lett., vol. 21, pp. 245-247, July 2000.
- [30]. Y. Taur, "Analytic-solutions-of-charge-and capacitance-in-symmetric and asymmetric double-gate MOSFETs," *IEEE Trans. Electron Devices*, vol. 48, p. 2861, Dec. 2001.
- [31]. Y. Taur et al., "A continuous, analytic drain-current model for DG-MOSFETs," IEEE Electron Device Lett., vol. 25, pp. 107-109, Feb. 2004.
- [32]. J. R. Hauser and M. A. Littlejohn, "Approximations for accumulation and inversion – space-charge layers in semiconductors," *Solid-State Electronics*, vol. 11, p. 667, 1968.
- [33]. M. Shoji and S. Horiguchi, "Electronic structures and phonon limited electron mobility of double-gate silicon-on-insulator silicon inversion layers," J. Appl. Phys., vol. 85, pp. 2722–2731, 1999.
- [34]. H. C. Pao and C. T. Sah, "Effects of diffusion current on characteristics of metal-oxide (insulator)—semiconductor_transistors," *Solid-State-Electron.*, vol. 9, p. 927, 1966.
- [35]. S. Datta, "Nanoscale device modeling: the green's function method," Superlattices and Microstructures, vol. 28, No. 4, pp. 253-278, 2000.
- [36]. M. P. Anantram, M. S. Lundstrom, and D. E. Nikonov, "Modeling of Nanoscale Devices", arXiv:cond-mat/0610247v2, Feb. 2007.
- [37]. S. Hriguchi, Physica B 227, 336, 1996.
- [38]. R. Venugopal, M. Paulsson, S. Goasguen, S. Datta, and M. Lundstrom, "A simple quantum mechanical treatment of scattering in nanoscale transistors," *J.Appl. Phys.*, vol. 93, pp. 5613-5623, 2003.

- [39]. R. Venugopal, Z. Ren, D. Jovanovic, S. Datta, and M. Lundstrom, "Simulating quantum transport in nanoscale mosfets: Real vs. mode space approaches," J.Appl. Phys., vol. 92, pp. 3730-3739, 2002.
- [40]. E. Polizzi and S. Datta, Proceedings of the 2003 third IEEE Conference—on______ Nanotechnology, 12-14 August 2003, p.-40.
- [41], R. Venugopal, PhD Thesis, Purdue University, USA, 2003.
- [42]. P. V. Halen-and-D. Pulfrey, "Accurate, short-series_approximations_to_fermi-dirac integrals of order -1/2, 1/2, 1, 3/2, 2, 5/2, 3, and 7/2", J. Appl. Phys., vol. 57, pp. 5271-5274, 1985.
- [43]. E. Polizzi and N. B. Abdallah, Physical Review B 66, 245301, 2002.
- [44]. J. Jin, "The Finite Element Method in Electromagnetics", John Wiley & Sons, Inc., New-York, USA, 1993.
- [45]. Z. Ren, R. Venugopal, S. Goasguen, S. Datta and M. Lundstrom, "nanoMOS 2.5: A-two dimensional simulator for quantum transport in double-gate MOSFETs", IEEE Trans. Elec. Devices, vol. 50, p. 1914, 2003.
- [46]. W.B. Joyce and R.W. Dixon, "Analytic approximations for the Fermi energy of an ideal Fermi gas," *Appl. Phys. Lett.*, 31, pp. 354-356, 1977.
- [47]. M. Cahay, M. McLennan, S. Datta and M. Lundstrom, "Importance of space charge effects in resonant tunneling devices", Appl. Phys. Lett. vol. 50, p. 612, 1987.
- [48]. F. Venturi, R. K. Smith, E. C. Sangiogri, M. R. Pinto, and B. Ricco, "A general purpose device simulator coupling Poisson and Monte Carlo transport with applications to deep submicron MOSFETs," IEEE Trans. Computer-Aided Design, vol. 8, p. 360, 1989.
- [49]. Z. Ren, PhD Thesis, Purdue University, USA, 2001.
- [50]. S. Jallepalli, J. Bude, W.-K. Shih, M. R. Pinto, C. M. Maziar and A. F. Tasch, "Electron and hole_quantization_and their impact_on_deep_submicron_silicon_p-_and_n-MOSFET characteristics," IEEE Trans. Electron Devices, vol. 44, pp. 297-302, 1997.
- [51]. http://www.nanohub.org/nanomos-
- [52]. J. Cai and C.-T. Sah, "Gate tunneling currents in ultrathin oxide metal-oxide silicon transistors," J. Appl. Phys., 89, pp. 2272-2285, 2001.

- [53]. E.M.Vogel, K.Z. Ahmed, B. Hornung, W.K. Henson, P.K. McLarty, G. Lucovsky, J.R. Hauser and J.J. Wortman, "Modeled Tunnel Currents for High Dielectric Constant Dielectrics," *IEEE Trans. Electron Devices*, 45, pp. 1350-1354, 1998.
- [54]. J. Wang, M. Lundstrom and E. Polizzi, "A three-dimensional quantum simulation of silicon nanowire transistors with the effective-mass_approximation," J. Appl. Phys., 96, pp. 2192-2203, 2004.
- [55]. N.C. Greenham- and R.H. Friend, "Semiconductor Device Physics of Conjugated Polymers", Solid State Physics 49, ed. H. Ehrenreich and F. Spaepen, Academic Press, New York, 1995.
- [56]. C. Dekker, "Carbon Nanotubes as Molecular Quantum Wires," *Phys. Today* 52, pp. 22-28, May, 1999.
- [57]. M.A. Reed, "Molecular-scale electronics," Proc. IEEE, 87, pp. 652-658, 1999.
- [58]. J. Taylor, H. Guo and J. Wang, "Ab_initio modeling_of quantum transport-properties-of molecular electronic devices," Phys. Rev. B, 63, 245407, pp.1-13, 2001.
- [59]. D.C. Langreth and J.W. Wilkins, "Theory of Spin Resonance in Dilute Magnetic Alloys," *Phys. Rev. B*, 6, pp.3189-3227, 1972.

 $\langle \cdot \rangle$

Appendix A

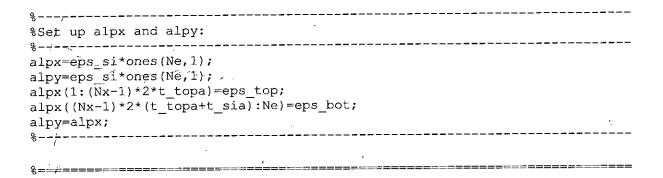
A.1 2D mesh generation-using MATLAB-

```
function mesh_dqmos2d
%mesh dgmos2d - 2d mesh generator function for rectangular DGMOSFET
%This function is used in FEM solution of 2D Poisson equation
2
%Related parameters:
%Lsd: drain_or_source_length
%Lg top : gate length
%t_top: top oxide thickness
%t bot: bottom oxide thickness
%t si: silicon thickness
%dx: x-grid spacing
%dy: y-grid spacing
%ns: no. of smaller grids
9
%Nn: number of nodes
%Ne: number of (triangular) elements
%x : x-coordinate of nodes (nex1)
%y : y-coordinate of nodes (nex1)
%te: connectivity matrix (3xne)
%Nb: no. of boundary points
%td: array of global number of Nb's
1
global Lsd Lg_top Lg_bot t_top t_bot t_si dx dy ns dys
global Lsda Lg topa Lg bota t_topa t_bota t_sia
global Nn Ne Nx Ny Nb x y te td yrow
global eps top eps bot eps_si alpx alpy
%Set up Nn,Ne,x,y:
8-----
                                          _____
dys=dy/2;
Lsd=round(Lsd/dx)*dx;
Lq top=round(Lg top/dx)*dx;
Lg_bot=round(Lg_bot/dx)*dx;
Lx=round((2*Lsd+Lg_top)/dx)*dx;
t top=round(t_top/dys)*dys;
t bot=round(t bot/dys)*dys;
t si=round(t_si/dys)*dys;
Ly=round((t_top+t_si+t_bot)/dy)*dy;
읏
Lsda=round(Lsd/dx);
Lg topa=round(Lg_top/dx);
Lg_bota=round(Lg_bot/dx);
Nx=round(Lx/dx)+1
t topa=2*ns+round((t top-2*ns*dys)/dy);
t bota=2*ns+round((t_bot-2*ns*dys)/dy);
```

```
t sia=2*ns+round((t si-2*ns*dys)/dy);
Ny=6*ns+round((Ly-6*ns*dys)/dy)+1
Nn=Nx*Ny
Ne=2*(Nx-1)*(Ny-1);
x=zeros(Nn,1);
y=zeros(Nn,1);
for i=1:Nx
   xcol(i) = (i-1) * dx;
end
x=repmat(xcol',Ny,1);
for_j=1:Ny_
   if j<=ns+1
       yrow(j) = (j-1) * dys;
    elseif j>=(t topa+1-ns+1)&j<=(t topa+1+ns)</pre>
       yrow(j)=yrow(j-1)+dys;
   elseif j>=(t topa+t sia+1-ns+1)&j<=(t topa+t sia+1+ns)</pre>
       yrow(j)=yrow(j-1)+dys;
   elseif j>=(Ny-ns+1)&j<=Ny-
       yrow(j)=yrow(j-1)+dys;
    else
       yrow(j)=yrow(j-1)+dy;
   end
end
yrow=yrow';
yd=repmat(yrow, 1, Nx);
y=reshape(yd', Nn, 1).;-
%_____
%Set up te:
8-----
                                 _____
te=zeros(3,Ne);
vel=[1;2;Nx+1];
ve2=[2;Nx+2;Nx+1];
for jj=1:(Ny-1)
    for ii=1:2:(2*(Nx-1))
       ee=(jj-1)*(2*(Nx-1))+ii;
       if ii==1
           te(:,ee) = ve1;
           te(:,_ee+1)=ve2;___
       else
           te(:,ee)=te(:,ee-2)+1;
           te(:,ee+1)=te(:,ee-1)+1;
       end
   .end
   /vel=vel+Nx;
   ve2=ve2+Nx;
        - . .
end
8-----
%Set up Nb,td:
%----
Nb=2*(Lg topa+1);
td_top=[Lsda+1:Lsda+Lg_topa+1]';
(Nn-(Lsda+Lg_topa+1)+1);
td_bot=[_(Nn-(Lsda+Lg_topa+1)+1):(Nn-(Lsda+1)+1)]';
td=[td top;td bot];
```

```
60
```

٠



A.2 Flow chart of the self-consistent simulation

