BANGLADESH UNIVERSITY OF ENGINEERING AND TECHNOLOGY, DHAKA

# L-3/T-2 B. Sc. Engineering Examinations 2010-2011 

Sub : IPE 493 (Industrial Management)

## Full Marks : 210

Time : 3 Hours
The figures in the margin indicate full marks.
USE SEPARATE SCRIPTS FOR EACH SECTION

## SECTION - A

There are FOUR questions in this section. Answer any THREE.

1. (a) State any five contemporary models of motivation. Explain Maslow's need theory.
(b) A pharmacist has been monitoring sales of a certain over the counter pain reliever.

Daily sales during the last 15 days were

| Day: | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Number <br> sold: | 36 | 38 | 42 | 44 | 48 | 49 | 50 | 49 | 52 | 48 | 52 | 55 | 54 | 56 | 57 |

(i) Which method would you suggest using to predict future sales-a linear trend equation or trend adjusted exponential smoothing? Why?
(ii) Assume that the data refer to demand rather than sales. Using trend adjusted smoothing with an initial forecast of 50 for week 8 , an initial trend estimate of 2 and $\alpha=\beta=0.3$, develop forecasts for days 9 through 16. What is the MSE for the eight forecasts for which there are actual data?
2. (a) Teri Hall has recently opened Sheer Elegance, Inc., a store specializing in fashionable stockings. Ms. Hall has just completed a course in managerial accounting and she believes that she can apply certain aspects of the course to her business. She is particularly interested in adopting the cost-volume profit (CVP) approach for decision making. Thus she has prepared the following analysis:

| Sales price per pair of stockings | $\$ 2.00$ |
| :--- | ---: |
| Variable expense per pair of stockings | 0.80 |
| Contribution margin per pair of stockings | $\$ 1.20$ |
| Fixed expenses per year |  |
| $\quad$ Building rental | $\$ 12,000$ |
| $\quad$ Equipment depreciation | 3,000 |
| $\quad$ Selling | 30,000 |
| $\quad$ Administrative | 15,000 |
| Total fixed expense | $\$ 60,000$ |

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## IPE 493 (EEE)

## Contd...O. No. 2(a)

## Required:

(i) How many pairs of stockings must be sold to break even? What does this represent in total dollar values?
(ii) Prepare a CVP graph for the store from a zero level of activity upto 70,000 pairs of stockings sold each year. Indicate break even point on the graph.
(iii) How many pairs of stocking must be sold to earn a $\$ 90,000$ target profit for the first year?
(iv) Mr. Hall has now 1 full time and 1 part time sales person working in the store. It will cost her an additional $\$ 8,000$ per year to convert the part time position to a full time position. Mr. Hall believes that the change would bring in an additional \$20,000 in sales each year. Should she convert the position?
(v) Refer to the original data. Actual operating results for the first year are as follows:

| Sales | $\$ 125,000$ |
| :--- | ---: |
| Less variable Expense | 50,000 |
| Contribution Margin | 75,000 |
| Less fixed expense | 60,000 |
| Net operating income | $\$ 15,000$ |

(a) What is the store's degree of operating of leverage? (b) Mr. Hall is confident that with some effort she can increase sales by $20 \%$ next year. What would be the expected percentage increase in net operating income?
(b) Briefly define chain of command and span of control. Discuss the differences between wide span of control and narrow span of control.
3. (a) What is contingency approach to leadership? According to Hersey and Blanchard's leadership model what are the 4 phases through which the relationship between managers and followers moves? State with figure. Fiedler measured leadership style on a scale what was that? Briefly explain.
(b) A small manufacturing firm uses roughly 3400 pounds of chemical dye a year. Currently the firm purchases 300 pounds per order and pays $\$ 3$ per pound. The supplier has just announced that orders of 1000 pounds or more will be filled at a price of $\$ 2$ per pound. The manufacturing firm incurs a cost of $\$ 100$ each time it submits an order and assigns an annual holding cost of $\$ 17 \%$ of the purchase price per pound. (i) Determine the order size that will minimum the total cost. (ii) If the supplier offered the discount at 1500 pounds, what order size would minimize total cost?
(c) State the basic objectives of 7 tools of TQM along with their interpretation.

## IPE 493 (EEE)

4. (a) Differentiate between efficiency and effectiveness. Briefly explain the four basic functions of managers.
(b) The following tasks must be performed on an assembly line in the sequence and times specified:

| Task | Task Time (seconds) | Task that must precede |
| :---: | :---: | :---: |
| A | 50 | -- |
| B | 40 | $\cdots$ |
| C | 20 | A |
| D | 45 | C |
| E | 20 | C |
| F | 25 | D |
| G | 10 | E |
| H | 35 | B, F, G |

(i) Draw the schematic diagram.
(ii) What is the theoretical minimum number of stations required to meet a forecasted demand of 400 units per 8 hour day?
(iii) Find the balance that minimizes the number of workstations, subject to cycle time and precedence constraints and also calculate the efficiency of the line.

## SECTION - B

There are FOUR questions in this section. Answer any THREE.
5. (a) Three public investment alternatives are available: $\mathrm{A}_{1}, \mathrm{~A}_{2}$ and $\mathrm{A}_{3}$. Their respective total benefits, costs and initial costs are given in present worth as follows:

| Present worth | Proposals |  |  |
| :---: | :---: | :---: | :---: |
|  | $\mathrm{A}_{1}$ | $\mathrm{~A}_{2}$ | $\mathrm{~A}_{3}$ |
| I | $\$ 100$ | $\$ 300$ | $\$ 200$ |
| B | $\$ 400$ | $\$ 700$ | $\$ 500$ |
| $\mathrm{C}^{\prime}$ | $\$ 100$ | $\$ 200$ | $\$ 150$ |

Three alternatives have the same service life. Assuming no do-nothing alternative, which project would you select, based on the benefit-cost ratio on incremental investment?
(b) What do you mean by 'PLC' in marketing? Mention its different stages with appropriate examples.
(c) What are the scopes/entities in which marketing people are involved?

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## IPE 493 (EEE)

6. (a) You are presented with the summary of projected costs and annual receipts for new product line. Calculate the IRR for this investment opportunity.

| End of year | Net cash flow, \$ |
| :---: | :---: |
| 0 | $-450,000$ |
| 1 | $-42,500$ |
| 2 | $+92,800$ |
| 3 | $+386,000$ |
| 4 | $+614,600$ |
| 5 | $-202,200$ |

(b) Selling of Djuice sim (a product of Grameen Phone) follows bell shaped PLC curve.

Do you agree? Justify your answer.
(c) Mention different types of buying behavior based on the degree of buyer involvement with appropriate examples.
7. (a) What is the amount of 10 equal annual deposits that can provide five annual withdrawals, where a first withdrawal of $\$ 3,000$ is made at the end of year 11 and subsequent withdrawals increase at the rate of $6 \%$ per year over the previous year's if the interest rate is $6 \%$ compounded annually?
(b) What are the competitive strategies under which market follower conduct their marketing activities? Give appropriate examples.
(c) How societal marketing concept is different from marketing concept?
8. (a) Leading role of a manager decreases where as planning and organizing role increases in an ascending order of the management levels - why? Explain with figure.
(b)"The steeper the total sales curve in a CVP graph the more profit or net operating income occurs" -- Explain.
(c) Briefly explain Joseph Jusan's "Zone of indifference".
(d) According to classical approach what are the distinctive characteristics that an organization should possess to be the most efficient and effective one? Briefly explain.
(e) If exponential smoothing technique is used when data shows a trend pattern what will happen? Which forecasting technique is appropriate for this situation?

## BANGLADESH UNIVERSITY OF ENGINEERING AND TECHNOLOGY, DHAKA

L-3/T-2 B. Sc. Engineering Examinations. 2010-2011
Sub : EEE 331 (Random Signal and Processes)
Full Marks: 210
Time : 3 Hours
USE SEPARATE SCRIPTS FOR EACH SECTION
The figures in the margin indicate full marks.

## SECTION-A

There are FOUR questions in this Section. Answer any THREE.
Symbols have their usual meanings.
Table of Fourier Transform is supplied.

1. (a) A certain communication system transmits the signal $S(t)$ show in Fig. Q..1(a). At the receiver, the signal received is $R(t)$, where $R(t)=S(t)+N(t) ; N(t)$ being an additive white Gaussian noise with zero mean and variance $\sigma_{n}^{2}$. Determine the impulse response of the matched filter that provides maximum SNR at $t=3 \mathrm{t}_{0}$. Using the impulse response, find the output of the filter. Sketch the output and describe how the presence of the signal $S(t)$ can be detected from the output of the filter.
(b) A random process $\mathrm{X}(\mathrm{t})$ has the following autocorrelation function:

$$
\begin{equation*}
R_{X X}(\tau)=36+\frac{4}{1+\tau^{2}} \tag{20}
\end{equation*}
$$

Determine (i) $\mathrm{E}(\mathrm{X})$ and ii) $\sigma_{x}^{2}$. Verify whether the process is mean-ergodic in the meansquare sense.
2. (a) A random process $X(t)$ is given by

$$
X(t)=A \cos (t)+(B+1) \sin (t) ; \quad-\infty<t<\infty
$$

where $A$ and $B$ are independent random variables with $\mathrm{E}(\mathrm{A})=\mathrm{E}(\mathrm{B})=0$ and $\mathrm{E}\left(\mathrm{A}^{2}\right)=$ $E\left(B^{2}\right)=1$. Find if $X(t)$ is wide-sense stationary. Is it stationary in strict-sense?
(b) For the higpass filter shown in Fig. $\mathrm{Q} .2(\mathrm{~b})$, the input signal $\mathrm{X}(\mathrm{t})$ is given by

$$
\begin{equation*}
\text { white } X(t)=A \sin \left(w_{c} t+\theta\right)+N(t) \tag{20}
\end{equation*}
$$

where $N(t)$ is additive while Gaussian noise and $\theta$ is an uniformly distributed random variable over $[0,2 \pi]$. Assuming zero initial conditions determine (i) the mean and variance of the output $\mathrm{Y}(\mathrm{t})$, (ii) the autocorrelation function of of $\mathrm{Y}(\mathrm{t})$ and (iii) power spectral density of $Y(t)$.
Assume that $\omega_{c}$ is an arbitrary constant frequency, and the filter a linear time-invariant system.

## EEE 331

3. (a) The power spectral density of a random process is shown in Fig. Q. 3(a). Verify whether the process has a derivative in the mean-square sense.
(b) A wide-sense stationary continuous-time process $X(t)$ has the following autocorrelation function:

$$
R_{X_{C} X_{C}}(\tau)=2 e^{-4 i \tau \mid}
$$

$\mathrm{X}_{\mathrm{c}}(\mathrm{t})$ is sampled with a sampling period of 10 seconds to produce the discrete-time sequence $X(n)$. Obtain the power spectral density of $X(n)$.
The sequence $X(n)$ is then provided as input to a linear time-invariant system with impulse response

$$
h(n)=e^{-a n} u(n)
$$

Find the power spectral density of the output of the system.
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4. (a) Two random variables $X$ and $Y$ have the joint probability density function (pdf) which is given by

$$
f_{X Y}(x, y)=4 x y ; 0<x<1 \text { and } 0<y<1 .
$$

Let the random variable u be defined as $U=X+Y$
Determine (i) $f_{u}(u)$ and (ii) $\varphi_{u}(u)$.
(b) Two random variables X and Y have variances $\sigma_{X}^{2}=16$ and $\sigma_{Y}^{2}=36$. Given that

$$
\begin{equation*}
p_{X Y}=0.5 . \quad P_{x y}=0.50 \tag{20}
\end{equation*}
$$

Determine (i) $C_{X Y}$, (ii) if $X$ and $Y$ are orthogonal where $E(X)=-3$ and $E(Y)=4$, (iii) $\sigma_{z}^{2}$ where $\mathrm{Z}=\mathrm{X}+\mathrm{Y}$ and (iv) $\sigma_{W}^{2}$ where $\mathrm{W}=\mathrm{X}-\mathrm{Y}$.

## SECTION - B

There are FOUR questions in this Section. Answer any THREE.
5. (a) Consider the communication channel shown in Fig. Q. 5(a). The $x$ transition probabilities are as follows:
$\mathrm{P}_{0 / 0}=0.8, \mathrm{P}_{1 / 0}=0.1, \mathrm{P}_{\mathrm{E} / 0}=0.1, \mathrm{P}_{0 / 1}=0.2, \mathrm{P}_{1 / 1}=0.7$ and $\mathrm{P}_{\mathrm{E} / 1}=0.1$. Here $\mathrm{P}_{\mathrm{Y} / \mathrm{X}}$ means the probability that Y is received, given X is transmitted. If $P[X=0]=P[X=1]=0.5$, find (i) $P[Y=0], P[Y=1]$ and $P[Y=E]$, (ii) If 0 is received, the probability that the transmitted bit is 0 , (iii) If $E$ is received, the probability that 1 is transmitted and (iv) If 1 is received, the probability that 1 is transmitted.

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## ERE 331

## Contd O. No. 5

(b) An audio amplifier contains six transistors. It is known that two of these transistors are defective. However, which two transistors are defective, is not known. Suppose you remove three transistors to inspect. Let $X$ be the number of defective transistors found after inspection. Sketch the probability mass function ( pmf ) of X for $\mathrm{X}=0,1,2$ and 3 .
6. (a) State and prove central limit theorem. What is the significance of this theorem?
(b) A random variable X has the following pdf:

$$
f_{X}(x)=\left\{\begin{array}{l}
\frac{1}{7}, \quad-3<x<4 \\
0, \text { otherwise }
\end{array}\right.
$$

Determine (i) $P\{|X-1.5| \geq 2\}$ using Chebyshev inequality and (ii) $P\{X>3.5\}$ using
Markov inequality. Comment on the results.
7. (a) Let X and Y be independent Zero mean Gaussian random variables with $g_{X}^{2}=4$ and $a_{Y}^{2}=9$. Find the pdf of Z where $\mathrm{Z}=2 \mathrm{X}-3 \mathrm{Y}$ using the characteristic function of Z .
$60=9$
(b) Two random variables $X$ and $Y$ have the joint pdf given by

$$
f_{X, Y}(x, y)= \begin{cases}\frac{1}{\pi}, & x^{2}+y^{2} \leq 1  \tag{15}\\ 0, & \text { otherwise }\end{cases}
$$

Find whether X and Y are uncorrelated and independent.
8. (a) Two random variables X and Y have the joint pdf given by

$$
f_{X, Y}(x, y)=\frac{1}{2 \pi \sqrt{3}} e^{\frac{-\left(x^{2}+2 x y+4 y^{2}\right)}{6}},-\infty<x<\infty,-\infty<y<\infty
$$

Determine
(i) $f_{X}(x)$ and $f_{Y}(y)$,
(ii) $E(X), E(Y), \quad \sigma_{X}^{2}$ and $\sigma_{Y}^{2}$
(iii) $f_{X / Y}(x / y)$ and $f_{Y / X}(y / x)$
(iv) $\operatorname{Cov}(X, Y)$ and $f_{y} \cdot P_{n}$
(b) A random variable has the following pdf

$$
f_{X}(x)=\left\{\begin{array}{l}
K\left(1-x^{4}\right) ;-1 \leq x \leq 1  \tag{15}\\
0, \text { otherwise }
\end{array}\right.
$$

ERE 331
Contd O. No. 8(b)

Find
(i) $F_{X}(x)$,
(ii) Value of K ,
(iii) $P\left[X<\frac{1}{2}\right]$,
(iv) $p\left[-\frac{1}{2}<x \leq \frac{1}{2}\right]$.




Fig. Q.2(6)

page-5

Table 8.1 Some Common Fourier Transform Pairs


> page -6
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Date : 19/11/2012

## BANGLADESH UNIVERSITY OF ENGINEERING AND TECHNOLOGY, DHAKA

L-3/T-2 B. Sc. Engineering Examinations 2010-2011
Sub : EEE 309 (Communication Theory)
Full Marks: 210
Time : 3 Hours
USE SEPARATE SCRIPTS FOR EACH SECTION
The figures in the margin indicate full marks.

## SECTION - A

There are FOUR questions in this Section. Answer any THREE.

1. (a) Discuss the limitations of a communication channel and their effects on the performance of a communication link.
(b) Distinguish between baseband transmission and bandpass transmission. Draw the block diagram of a baseband transmission system and explain how the distortion occurs in such a system. Why are repeaters used in such a system?
(c) Derive the Nyquist condition for distortionless transmission through a baseband transmission system and explain briefly.
2. (a) What is meant by multiplexing? State the purpose of multiplexing. Briefly discuss the principles of FDM and TDM. Distinguish between asynchronous, plesiochronous and synchronous TDM.
(b) With necessary schematic diagram, discuss the CCITT recommended FDM schemes for generation of FDM groups, Super groups and Master groups.
(c) $16 \mathrm{~T}_{1}$ (DS1) channels are time multiplexed with $8 \mathrm{E}_{1}$ channels and a data channel as shown in the following diagram. Bandwidth available for transmission over the channel is 100 MHz . Determine the allowable bit rate $R_{b_{3}}$ of the data channel, $R_{b_{1}}, R_{b_{2}}$ and output bit rate, $R_{0}$. Assume channel spectral efficiency of $1 \mathrm{bit} / \mathrm{sec}-\mathrm{Hz}$.

3. (a) Define amplitude modulation (AM) and the index of AM. Write an expression of the AM signal for a single sinusoid as the modulating signal and find the expression of the spectrum of the AM signal.
(b) The modulating signal input to a square-law modulator is given by $\rho_{m}(t)=10 \sin \left(2 \pi \times 10^{3} t\right)+5 \cos \left(2 \pi \times 10^{4} t\right)+3 \cos \left(4 \pi \times 10^{4} t\right)$. The carrier is a sinusoid with frequency 1 MHz and peak amplitude 24 Volt. The output of the square-law modulator is passed through a BPF to generate DSB signal. Draw the spectrum of the signal at the input and output of the BPF and determine:
(i) bandwidth of the BPF filter;
(ii) the overall modulation index;
(iii) power in the sidebands;
(iv) total power in the DSB signal.
(c) What are meant by heterodyne and coherent detection? Draw the block diagram of a superheterodyne AM envelope detection receiver and show the waveforms at different output stages.
4. (a) What is meant by angle modulation and what are its different forms? Write expressions for FM and PM signals and show how an FM signal can be generated using a PM modulator.
(b) Distinguish between narrowband and wideband FM. With necessary block diagram discuss the Armstrong method of generating a WB-FM signal starting with a NB-phase modulator.
(c) An FM radio channel is operating at a carrier frequency of 96.2 MHz with a channel bandwidth of 0.2 MHz . The bandwidth of the modulating signal is 15 kHz . The peak frequency deviation is 60 kHz . The carrier amplitude is 100 V rms.

## Determine:

(i) the modulation index;
(ii) bandwidth of the FM signal using Carson's rule.
(iii) number of sidebands available within the channel bandwidth;
(iv) power in the sidebands
(v) total output power of the FM transmitter.

Use Bessel Function table.

## SECTION - B

There are FOUR questions in this section. Answer any THREE.
Answer in brief and to the point.
5. Answer the following short questions:
(a) What was the basic purpose of sampling an analog signal that results in a PAM signal? With neat sketches, define natural and flat-topped sampling.
(b) State the famous sampling theorem. Determine the Nyquist's sampling frequency for the message signal $x(t)$ represented by

$$
\begin{equation*}
x(t)=-2.5 \sin (1000 \pi t)+0.7 \cos (6800 \pi t)+1.3 \cos (2000 \pi t) \tag{4}
\end{equation*}
$$

(c) Mention how you can reconstruct the original analog signal from its sampled version.
(d) Draw the basic block diagram of a PCM transmitter. What is the function of a sampler in PCM? What type of sampler is used here and why?
(e) Draw the binary-encoded digital pulse-train (voltage vs. time) corresponding to the bit-stream 10110011 at the output of the PCM transmitter. Also, draw the approximate waveshapes of the same pulse train at a distance of 2 kms from the transmitter. (Assume Cu -wire subject to noise and attenuation as the medium). Explain the causes for the change in shapes of the pulse train.
(f) Explain how the effect of white noise can be totally eliminated in a PCM system.
(g) Define quantization noise in PCM and state its effect on the quality of the received signal. How can we minimize quantization noise?
6. (a) Calculate the bit-rate of a baseband digital signal in a single channel PCM system that uses the standard band-limited telephone channel. What is the equivalent bandwidth of this signal? Can we ideally pass this signal through an analog band-limited telephone channel ( $300 \mathrm{~Hz}-3400 \mathrm{~Hz}$ )? If not, why?
How can we modify the encoded digital signal so that it can be passed through the same band-limited channel with minimum signal distortion?
(b) Define the basic digital modulation schemes with neat sketches and examples. Mention their relative merits and demerits.
(c) Draw the synchronous detection circuit that can detect a PSK signal. Justify its proper operation using an appropriate example and mathematical relation. Can it be used for detecting an OOK signal? If so, explain how.

## EEE 309

7. (a) Comment on the bandwidth of human speech signals. In PCM, why do we still bandlimit the speech signal between $300 \mathrm{~Hz}-3400 \mathrm{~Hz}$ (an analog FDM band for a subscriber)?
Calculate the bit-rate of a 30 -channel PCM system (E1). Mention the purpose of source coding in digital communication.
(b) What is multi-level modulation and what was the basic purpose of its introduction?

Draw the block diagram of a QPSK modulator circuit ușing two BPSK modulators. Comment on the tasks of each of the two level shifters used. What are the phase states of the carrier when the bit-stream 10110001 is applied to the QPSK modulator?
(c) Draw the block diagram of a QPSK demodulator. With proper mathematical equations and tables show that the signal extracted at the output of the demodulator will be exactly 10110001 as transmitted from the QPSK modulator in part (b).
8. (a) Draw the block diagram of the simplest delta-modulator and demodulator, and explain their operations in brief. Mention the relative merit of delta modulation over PCM. On what type of signal can we apply delta modulation? What is granular/quantization noise in delta modulation? How can we minimize this noise?
(b) Define information and information capacity. Name and explain the two factors that limit the rate of information transmission through a communication channel.

A message consists of the letters A, B, C and D. Suppose that each letter is coded into a sequence of two binary (on-off) pulses. The letter A is represented by $00, \mathrm{~B}$ by $01, \mathrm{C}$ by 10 and D by 11. Each individual pulse interval is 5 ms . Calculate the average rate of information transmission if the different letters are equally likely to occur.
(c) Mention the essential properties of the set of orthogonal Pseudo-Noise (PN) codes to be used in DS-SS multiple access technique. Verify whether the following two sets of PN codes are orthogonal:

$$
\begin{aligned}
& x=\left[\begin{array}{llll}
-1 & -1 & 1 & 1
\end{array}\right] \text { and } \\
& y=\left[\begin{array}{llll}
-1 & (-1) & 1 & -1
\end{array}\right] .
\end{aligned}
$$

Draw the block diagram of a 2-channel DS-SS CDMA scheme. Mention the function of each block using brief statements.

AE 309 Communication Theory


For more extensive tables of Bessel functions, see Watson (1966, pp. 666-697), and Abramowitz and Stegun (1965, pp. 358-406).

# BANGLADESH UNIVERSITY OF ENGINEERING AND TECHNOLOGY, DHAKA 

## L-3/T-2 B. Sc. Engineering Examinations 2010-2011

Sub: EEE 315 (Microprocessor and Interfacing)
Full Marks: 210
Time: 3 Hours
The figures in the margin indicate full marks.
USE SEPARATE SCRIPTS FOR EACH SECTION

## SECTION - A

There are FOUR questions in this Section. Answer any THREE.

1. (a) Design an 8086 system with four $4 \mathrm{k} \times 8$ ROMs (address starting from 1 F 800 H ), eight $2 \mathrm{k} \times 4$ RAMS (address starting right after the last address of the ROMs) and one 8251A USART (base address $7800 \mathrm{H}, 7801 \mathrm{H}$ ). Implement the even - odd addressing. Show the memory map and the connection schematic.
(b) Sketch and explain the timing diagram of the read cycle of 8086.
2. (a) Interface an 8086 microprocessor to a 8 level paper tape reader through the PORT A of an 8255 A PPI in such a way that whenever data is read from the tape the 8255 A sends an interrupt to the IR6 pin of an 8259 PIC. If the base addresses of the 8255A are F009H, F00BH, F00DH and F00FH while the base addresses of the 8259 PIC are F00AH ad F 00 EH , respectively, draw the connection diagram of the system using DECODERS (IC74LS138) only.
(b)Write assembly codes to initialize the 8259 PIC and the 8255A-PPI properly so that there is a type 38 interrupt request when a data is read by the $8255 \mathrm{~A}-\mathrm{PPI}$. Initialize the rest of the ports of the 8255 A in mode 0 .
(c) Write a code snippet for the function called during the interrupt so that data is read from the tape connected to the PORT A of the 8255A.

## 8259 Control Words

|  | $\mathrm{A}_{0}$ | $\mathrm{D}_{7}$ | D6 | $\mathrm{D}_{5}$ | D 4 | $\mathrm{D}_{3}$ | $\mathrm{D}_{2}$ | $\mathrm{D}_{1}$ | $\mathrm{D}_{0}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| ICW1 | 0 | $\mathrm{A}_{7}$ | $\mathrm{A}_{6}$ | $\mathrm{A}_{5}$ | 1 | LTIM | ADI | SNGL | IC4 |
| ICW2 | 1 | $\mathrm{A}_{15} / \mathrm{T}_{7}$ | $\mathrm{A}_{14} / \mathrm{T}_{6}$ | $\mathrm{A}_{13} / \mathrm{T}_{5}$ | $\mathrm{A}_{12} / \mathrm{T}_{4}$ | $\mathrm{A}_{11} / \mathrm{T}_{3}$ | $\mathrm{A}_{10}$ | $\mathrm{A}_{9}$ | $\mathrm{A}_{8}$ |
| ICW3 | 1 | $\mathrm{S}_{7}$ | $\mathrm{S}_{6}$ | $\mathrm{S}_{5}$ | $\mathrm{S}_{4}$ | $\mathrm{S}_{3}$ | $\mathrm{S}_{2}$ | $\mathrm{S}_{1}$ | $\mathrm{S}_{0}$ |
| ICW4 | 1 | 0 | 0 | 0 | SFNM | BUF | M/S | AEOI | $\mu \mathrm{PM}$ |
| OCW1 | 1 | $\mathrm{M}_{7}$ | $\mathrm{M}_{6}$ | $\mathrm{M}_{5}$ | $\mathrm{M}_{4}$ | $\mathrm{M}_{3}$ | $\mathrm{M}_{2}$ | $\mathrm{M}_{1}$ | $\mathrm{M}_{0}$ |
| OCW2 | 0 | R | SL | EOI | 0 | 0 | $\mathrm{L}_{2}$ | $\mathrm{L}_{1}$ | L0 |

3. (a) Describe the functions of the $\overline{D S R}, \overline{D T R}, \overline{R T S}, \overline{C T S}, \mathrm{TxD}$ and RxD signals exchanged between a terminal and a modem.
(b) Write down the instructions of transmitting and receiving data through 8251 A using polling method if the control register and data register addresses of the 8251 are FFF2H and FFFOH , respectively.

## EEE 315

## Contd... Q. No. 3(b)

## 8251 Control Words

| WNW | S2 | S1 | EP | PEN | L2 | L1 | B2 | B1 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| CWW | EH | IR | RTS | ER | SBRK | RxE | DTR | TxEN |
| SWW | DSR | SYNDET | FE | OE | PE | TXE | RXRDY | TXRDY |

(c) Why must you use an IRET instruction rather than the regular RET instruction at the end of an interrupt services routine?
(d) What response will an 8086 microprocessor show if it receives a divide-by-zero interrupt exactly at the same time when an NMI occurs? Why?
(e) What is the purpose of interrupt vector table? What is its range in the SDK Board? How many interrupts can it accommodate?
4. (a) Draw a circuit diagram using timer 0 of an 8254 timer/counter IC that can be used to determine power line failure for an 8086 system considering the internal addresses of the 8254 to be FF00H, FF01H, FF10H and FF11H, respectively. If the power line signal is $220 \mathrm{~V}, 50 \mathrm{~Hz}$ and the 8254 is connected to a 1 MHz clock, what can be a possible count value loaded to the counter and why? In which mode will you operate the counter? Write an assembly code to initialize the counter.

## 8254 Control Words

| SC1 | SC0 | RW1 | RW0 | M2 | M1 | M0 | BCD |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

(b) Describe all the methods of reading the count value from the 8254 counter.

## SECTION-B

There are FOUR questions in this Section. Answer any THREE.
5. (a) Design a system with $28 \times 8$ LED matrix, shown in figure for $Q 5(a)$, and a microcontroller, so that the system can display a $8 \times 16$ image. Also, write a program for that microcontroller system that can display an $8 \times 16$ image stored in the memory in the following manner: Each 8 LED row is represented by an 8 bit unsigned integer, where each bit of the integer represents the ON or OFF state, total 16 integers are given in an array A , representing each row. Use appropriate delays in your code, and in comment, explain why the particular delay values were used.
Char A[]$=\{0 \times \mathrm{xAB} 0 \times B C \quad \ldots\} / /$ image array
(you do not need to declare A, assume it is already defined in code. Also, do not redraw the LED matrix, and use bubbles to show connections)

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## EEE 315

## Contd... O. No. 5(a)



Figure for Question 5(a)
(b) For the given H -bridge motor driver, design a microcontroller system, that has 2 switches. Pressing switch no 1 will toggle the On/Off state of the motor. Pressing switch no 2 will reverse the direction of rotation of the motor. For your design, use the p1, p2, p3 and p 4 bubbles, and do not redraw the H -bridge. Write appropriate code for the system.


Figure for Question 5(b)
(c) What are the differences between a microprocessor and a microcontroller?
6. (a) The following statements are part of a continuous program. Fill up the values of the appropriate flags after sequential execution of each instruction.
(i) $\mathrm{ADD} \mathrm{AX}, \mathrm{CX}$
(ii) $\mathrm{ADD} \mathrm{BX}, \mathrm{AX}$
(iii) SUB BX, 1 h
(iv) INC BL
(v) NEG DX
(vi) DIV CX
(vii) TEST DX, DX

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## EEE 315

## Contd ... O. No. 6(a)

|  | ZF | SF | CF | OF | PF | AX | BX | CX | DX |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Initial | $\mathbf{0}$ | $\mathbf{0}$ | $\mathbf{0}$ | $\mathbf{0}$ | $\mathbf{0}$ | $\mathbf{0 0 0 0}$ | FFFFh | $\mathbf{8 0 0 1 h}$ | $\mathbf{0 0 0 0 h}$ |
| i. |  |  |  |  |  |  |  |  |  |
| ii. |  |  |  |  |  |  |  |  |  |
| iii. |  |  |  |  |  |  |  |  |  |
| iv. |  |  |  |  |  |  |  |  |  |
| v. |  |  |  |  |  |  |  |  |  |
| vi. |  |  |  |  |  |  |  |  | . |
| vii. |  |  |  |  |  |  |  |  |  |

(b) A DB $\mathrm{r}, \mathrm{t}$ is a complex number given in polar form ( $\mathrm{r} \angle \mathrm{t}$ ). r and t are arbitrary constants in decimal form, $t$ given in degree. Write an assembly program that will multiply two such complex numbers $A$ and $B$ and put the result in $M$ and divide $A$ by $B$ and put the result in $D$, where $B, M$ and $D$ are also polar form complex number defined in the same manner as A .
(c) The address C82F:1000 contains an instruction. (i) What should be the value of IP for a program to execute that instruction, if value of CS for that program is C000h? (ii) What can be the minimum value of CS of a particular program, for which the instruction C82F:1000 can be executed by that program, without help of any jump commands.
7. (a) For each of the following statements state the addressing mode of the source and destination operand. If the instructions are executed sequentially, write the content of the memory after execution of all the instructions.

| Memory Offset | Content |
| :--- | :--- |
| 1000 h | 12 h |
| 1001 h | 45 h |
| 1002 h | 34 h |
| 1003 h | 67 h |
| 1004 h | 78 h |
| 1005 h | 17 h |
| 1006 h | 23 h |

Instructions:
(i) MOV BX, 1003 h
(ii) MOV AX, $[B X]$
(iii) MOV SI, 1004h
(iv) MOV DI, SI
(v) XCHG [SI], [DI]
(vi) MOV [SI], [1005]

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## EEE 315

## Contd ... Q. No. 7

(b) Consider the following code. What will be the value of R after execution of the code? The code performs a function. State what is that function.
MOV AL, 1
MOV BL, N
CALL Procl
MOVAL,R MOV R,AL

HLT
Procl:
TEST BL, OFFh
JZ, rett: IZ vet1
MUL BL
DEC BL
CALL Proc 1
ret1 : RET
NDB 5
R DB?
HLT
(c) Define with examples: (any three)
(i) Immediate Addressing
(ii) Unconditional Jump
(iii) Register Indirect Addressing
(iv) Divide Overflow
8. (a) A palindrome is a word or phrase that may be read the same way in either direction. (Example "MADAM IM ADAM"). Write an assembly program that will test a string whether it is a palindrome. (spaces are ignored while testing for palindrome)
(b) In the assembly code given in table, there are some syntax as well as logical errors. Find the errors and correct them with proper explanation. Note that the first of the two erroneous statements is always correct, and all other consecutive statements must be corrected if necessary. Provide the corrections in a tabular form given in the Figure for Question 8(b).

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## EEE 315

## Contd ... O. No. 8(b)

| Line No | Correct Instruction | Explanation |
| :---: | :---: | :---: |
| $\vdots$ |  |  |
| $\vdots$ |  |  |
| $\vdots$ |  |  |
| $\vdots$ |  |  |
| $\vdots$ |  |  |
| $\vdots$ | Answer to be written | Answer to be written <br> inside the table in <br> inside the table in |
| $\vdots$ | answer script |  |
| $\vdots$ |  |  |
| $\vdots$ |  |  |
| $\vdots$ |  |  |
| $\vdots$ |  |  |
| $\vdots$ |  |  |
| $\vdots$ |  |  |

Figure for Question 8(b)

ALPHA SEGMENT
ASSUME CS : CODE, DS : CODE
MOV DX, AH
MOV AX, 12
MOV BX, [AX]
MOV AL, [BL]
ADD AL, [SI]
ADD CX, [DI]
PUSH AL
PUSHF
ALPHA ENDS
(c) Write two separate assembly codes each of which would generate infinite loop in an 8086.

Date : 17/12/2012
BANGLADESH UNIVERSITY OF ENGINEERING AND TECHNOLOGY, DHAKA
L-3/T-2 B. Sc. Engineering Examinations 2010-2011
Sub : EEE 371 (Power System II)
Full Marks: 210
Time : 3 Hours
USE SEPARATE SCRIPTS FOR EACH SECTION
The figures in the margin indicate full marks.

## SECTION - A

There are FOUR questions in this Section. Answer any THREE.
All the symbols have their usual meanings.

1. (a) Using the concept of synchronizing power coefficient, show that the operating point of an alternator, $\delta \leq 90^{\circ}$ for a stable system.
(b) Consider the following single line diagram. All impedances are in Pu.


The machine is delivering 1.0 pu. power and both the terminal and infinite bus voltages are 1.0 pu. Determine (i) the power-angle equation of the system, (ii) corresponding swing equation if $P_{m}=1.0$ p.u., (iii) operating point of the system and (iv) the value of synchrononizing power coefficient.
(c) What are the factors that affect the transient stability of a system?
2. (a) What is sub-synchronous resonance? Show that $f_{r}=f \sqrt{k}$.
(b) Determine the value of series capacitor to increase the power transfer capability of a transmission line by 3 times. Given that the inductive reactance of the line is $5 \Omega$ and frequency is 50 Hz .
(c) Consider two buses, $\mathrm{B}_{1}$ and $\mathrm{B}_{2}$, with voltages $V_{1} \angle-2^{\circ}$ pu. and $V_{2} \angle 0^{\circ}$ pu. Using a phase shifting transformer, the voltage of $\mathrm{B}_{1}$ is changed to $V_{1} \angle-3^{\circ}$ p.u. Determine the $\%$ increase of power flow between buses.
(d) For the following table, calculate $\%$ TDD if $\mathrm{I}_{\mathrm{L}}=100 \mathrm{~A}$.

| Harmonic order | Individual current harmonic (A) |
| :---: | :---: |
| 2 | 0.2 |
| 3 | 0.4 |
| 4 | 0.6 |
| 5 | 0.8 |

## SEE 371

3. (a) For the following single line diagram, explain and derive the equal area criterion.

(b) For the network shown in Fig. 3(a), derive the expressions of (i) critical clearing angle and (ii) critical clearing time. Also explain their significances.
4. (a) For a power system, the power angle equations are,

Before the fault: $\mathrm{P}_{\max } \sin \delta=2.10 \sin \delta$
During the fault: $\mathrm{r}_{1} \mathrm{P}_{\max } \sin \delta=0.808 \sin \delta$
After the clearance of fault: $\mathrm{r}_{2} \mathrm{P}_{\max } \sin \delta=1.50 \sin \delta$
Mechanical input, $\mathrm{P}_{\mathrm{m}}=1.0$ p.u.
Determine the critical clearing angle of the system.
(b) For an overhead transmission line, considering span $=\mathrm{L}$, weight/ unit length of conductor $=\mathrm{W}$ and horizontal tension $=\mathrm{H}$, derive the hyperbolic equation of length of the conductor, catenary curve, sag and tension.
(c) What is FACTS technology? Write down its advantages.

## SECTION - B

There are FOUR questions in this Section. Answer any THREE.
5. (a) Derive the expression for inductance of a single phase composite conductor transmission line interms of GMD and GMR.
(b) The distance between conductors of a single phase line is 10 ft . Each of its conductors is composed of six strands symmetrically placed around one center strand (total 7 strands). The diameter of each strand is 0.1 Show that $D_{s}$ of each conductor is 2.177 times the radius of each strand. Find the inductance of the line in $\mathrm{mH} / \mathrm{mi}$.
(c) Explain, why bundled conductor lines have lower inductance than single-conductor lines of the same area of cross-section.

## LE 371

6. (a) Derive an expression for the capacitance to neutral in farads per meter of a singlephase line, taking into account the effect of ground.
(b) The line in Q. 6(a) is composed of two solid circular conductors, each having a diameter of 0.229 in . The conductors are 10 ft apart and 25 ft above ground. Calculate and compare the capacitance to neutral per meter with and without the effect of ground.
(c) Six conductors of ACSR Drake constitute a $50-\mathrm{Hz}$ double-circuit $3 \phi$ line arranged as shown in the Fig. Find the capacitive reactance to neutral (in $\Omega$-mi) and the charging current in $\mathrm{A} / \mathrm{mi}$ per phase and per conductor at 132 kV . Consider $\mathrm{GMR}=0.0373 \mathrm{ft}$ and the outside diameter of the conductor as 1.108 in .

7. (a) Discuss stress distribution in a HVDC cable.
(b) Explain how capacitance between conductors $\left(\mathrm{C}_{\mathrm{c}}\right)$ and that between conductor and sheath can be calculated through various methods.
(c) A single-core 5 km long cable has an insulation resistance of $0.4 \mathrm{~m} \Omega$. The core diameter is 20 mm and diameter of the cable over the insulation is 50 mm . Calculate the resistivity of the insulating material.
8. (a) Explain with appropriate diagrams the various types of HVDC transmission systems.
(b) Present an economic comparison between a $3 \phi \mathrm{AC}$ line and a 2 -wire DC line.
(c) An existing $3 \phi$, double-circuit AC line is to be converted to three-circuit DC line.

Assuming same insulation level and unity pf. in the AC systems. Show that
(i) the ratio of power transmitted by DC to that by AC is equal to $\sqrt{2}$
(ii) the ratio of percentage loss by $D C$ to that by $A C$ is equal to $1 / \sqrt{2}$.

## L-3/T-2/EEE

Date : 17/12/2012

## BANGLADESH UNIVERSITY OF ENGINEERING AND TECHNOLOGY, DHAKA

L-3/T-2 B. Sc. Engineering Examinations 2010-2011
Sub : EEE 351 (Analog Integrated Circuits)
Full Marks: 210
Time: 3 Hours
USE SEPARATE SCRIPTS FOR EACH SECTION
The figures in the margin indicate full marks.

## SECTION-A

There are FOUR questions in this Section. Answer any THREE.
All the symbols and notations used in this part have their usual meanings.

1. (a) Briefly describe different types of noises and their origins those may corrupt signal processed by integrated circuits.
(b) Determine the input-referred noise voltage for the circuit shown in Fig. for Q . No. 1(b).

(c) For an NMOS current source, calculate the total noise in the drain current for a band from 100 Hz to 10 MHz .
2. (a) Given a short account of the methods for supply-independent biasing, temperatureindependent references and constant $G_{m}$ biasing for IDs. Your arguments should be supported by proper circuits and mathematics.
(b) Assume $\lambda \neq 0$, in Fig. for Q . No. 2(b), estimate the change in $\mathrm{I}_{\text {out }}$ for a small change in the supply voltage $V_{D D}$.

## EEE 351

## Contd O. No. 2(b)



Fig. for Q. No. 2 (b)
3. (a) Describe the operation of the unity-gain sampler in slow motion. Draw a scheme to co-ordinate the switches of the unity-gain sampler from a single clock.
(b) Draw the precision multiply-by-2 circuit of a non-inverting amplifier. Also illustrate the differential realization of non-inverting amplifier.
(c) Convert the continuous-time integrator shown in Fig. for Q . No. 3(c) into a discretetime integrator such that it becomes parasitic-insensitive.


Fig. for Q. No. 3(c)
4. (a) What are PD, FD and PFD? Implement a simple PLL in CMOS technology.
(b) Improve the charge-pump PLL such that it can be more dynamically stable and can reduce ripple on the control line.
(c) From the Fig. for Q. No. 4(c), draw the net current and the effect of up/down current mismatch on $\mathrm{V}_{\text {cont }}$ (in reference to CP-PLL).


Fig. for Q. No. 4(c)
(d) Mention some applications of PLL/DLL.

## EEE 351

## SECTION - B

There are FOUR questions in this Section. Answer any THREE.
5. (a) The drain-source current ( $I_{d s}$ ) of the NMOS transistor in linear region is given by the following equation where the symbols have their usual meaning.

$$
I_{d s}=\mu_{n} C_{O X}\left(\frac{W}{L}\right)\left[\left(V_{g s}-V_{t}\right) V_{d s}-\frac{1}{2} V_{d s}^{2}\right]
$$

Starting from the above equation derive the drain-source current of the NMOS transistor in the saturation region and show that it does not depend on the drain-source voltage.
(b) For the circuit shown in Fig. for Q. 5(b) sketch $\mathrm{I}_{\mathrm{X}}$ and the transconductance of the transistor as a function of $\mathrm{V}_{\mathrm{X}}$. The following parameters are given: $\lambda=0.1, \gamma=0.45$, $\phi_{\mathrm{F}}=0.45 \mathrm{~V}, \mathrm{~V}_{\text {THO }}=0.7 \mathrm{~V}, \mu_{\mathrm{n}} \mathrm{C}_{\text {ox }}=120 \mu \mathrm{~A} / \mathrm{V}^{2}, \mathrm{~W} / \mathrm{L}=10$.


Fig. for $Q 5$ (b)
(c) A designer has to use a large MOSFET transistor with gate width, $\mathrm{W}=200 \mu \mathrm{~m}$ and gate length, $\mathrm{L}=2 \mu \mathrm{~m}$ in a certain analog circuit. Finding that the capacitances become unacceptably large he decided to split it into 2 MOSFET connected in parallel. Show the layout of the MOSFET and calculate the drain body ( $\mathrm{C}_{\mathrm{DB}}$ ) and source body ( $\mathrm{C}_{\mathrm{SB}}$ ) capacitance of the transistor for both of the cases. The source and the body of the transistor is connected with ground potential and the drain is connected with 5 V supply. The following data are given: drain/source length $(\mathrm{E})=10 \mu \mathrm{~m}, \mathrm{C}_{\mathrm{j} 0}=0.15 \mathrm{pF} / \mathrm{m}^{2}$, $\mathrm{C}_{\mathrm{jsw} 0}=0.1 \mathrm{pF} / \mu \mathrm{m}, \phi_{\mathrm{B}}=0.6 \mathrm{~V}, \mathrm{~m}_{\mathrm{j}}=0.6, \mathrm{~m}_{\mathrm{jsw}}=0.4$, where the symbols have their usual meanings.
6. (a) For the common source amplifier with diode connected load as shown in Fig. for Q . 6(a), show that the voltage gain can be expressed as a function of the aspect ratio of transistors $M_{1}$ and $M_{2}$ and $\eta_{2}\left(g_{m b 2} / g_{m} 2\right)$, where the symbols have their usual meanings.


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## EEE 351

## Contd Q. No. 6

(b) Calculate the voltage gain $\mathrm{V}_{\text {out }} / \mathrm{V}_{\text {in }}$ of the cascade stage shown in Fig. for $\mathrm{Q} .6(\mathrm{~b})$.

Assume $\lambda=0$.


Now, in the above circuit assume $\mathrm{Rp}=\infty$ (infinity), and $\lambda=\gamma=0$, Also assume threshold voltage of $M_{1}$ and $M_{2}$ as $V_{T H 1}$ and $V_{T H 2}$, respectively. Sketch the transfer characteristics $\left(\mathrm{V}_{\text {out }}\right.$ vs. $\left.\mathrm{V}_{\text {in }}\right)$ and $\mathrm{V}_{\mathrm{x}}$ vs. $\mathrm{V}_{\text {in }}$ curve as $\mathrm{V}_{\text {in }}$ varies from 0 to $\mathrm{V}_{\mathrm{DD}}$.
7. (a) In the differential pair circuit shown in Fig. for Q. 7(a), the transistor $M_{1}$ and $M_{2}$ are identical having an aspect ratio of $W / L$. Assume $M_{1}$ and $M_{2}$ are saturated and $\lambda=0$. For the differential input voltage $\mathrm{V}_{\text {in } 1}$ and $\mathrm{V}_{\mathrm{in} 2}$, show that the overall transconductance of the differential pair $G_{m}$ defined as $G_{m} \frac{\partial \Delta I_{D}}{\partial \Delta V_{i n}}$ becomes maximum at $\Delta \mathrm{V}_{\text {in }}=0$ and calculate this maximum value of $G_{m}$. Also find the value of $\Delta \mathrm{V}_{\text {in }}$ for which $G_{m}$ falls to zero.


Fig. for $Q 7$ (a) and 7(b)
(b) In the circuit shown in Fig. for Q. 7(b), a common mode voltage $V_{c m}$ is applied at both $\mathrm{V}_{\text {in1 }}$ and $\mathrm{V}_{\mathrm{in} 2}$. Let $\mathrm{V}_{\mathrm{DD}}=3 \mathrm{~V}, \mathrm{~K}_{\mathrm{n}}^{\prime}(\mathrm{W} / \mathrm{L})=4 \mathrm{~mA} / \mathrm{V}^{2}$ for both transistors, $\mathrm{V}_{\mathrm{t}}=0.5 \mathrm{~V}$, $I_{S S}=0.4 \mathrm{~mA}$, and $\mathrm{R}_{\mathrm{DI}}=\mathrm{R}_{\mathrm{D} 2}=2.5 \mathrm{~K} \Omega$ and neglect channel length modulation.
(i) Find $V_{O V}$ and $V_{G S}$ for each transistor
(ii) For $V_{c m}=0$, find $\mathrm{V}_{\mathrm{S}}, \mathrm{I}_{\mathrm{D} 1}, \mathrm{I}_{\mathrm{D} 2}, \mathrm{~V}_{\mathrm{D} 1}$ and $\mathrm{V}_{\mathrm{D} 2}$
(iii) What is the highest value of $V_{c m}$ for which $\mathrm{M}_{1}$ and $\mathrm{M}_{2}$ remains in saturation. Make necessary assumptions.

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## EEE 351

8. (a) A basic current mirror circuit is shown in Fig. for Q. 8(a). Aspect ratio of $M_{1}$ and $M_{2}$ are $(\mathrm{W} / \mathrm{L})_{1}$ and $(\mathrm{W} / \mathrm{L})_{2}$, respectively and channel length modulation parameter of both transistor is $\lambda$. Find the drain current $\mathrm{I}_{\mathrm{D} 2}$ in terms of $\mathrm{I}_{\mathrm{D} 1}$.


Fig. for Q 8(a)
(b) In order to suppress the effect of channel length modulation, a cascode current source can be used. Show the circuit diagram of a cascode current source and explain its operation. Explain how cascode bias voltage $\mathrm{V}_{\mathrm{b}}$ can be generated.
(c) Show the circuit diagram of a differential amplifier with NMOS input and PMOS active current mirror load and a realistic steering current source ( $\mathrm{I}_{\text {SS }}$ ). Briefly explain the operation of the circuit.

